

FIG. 1



Fig. 2

09836781.041701

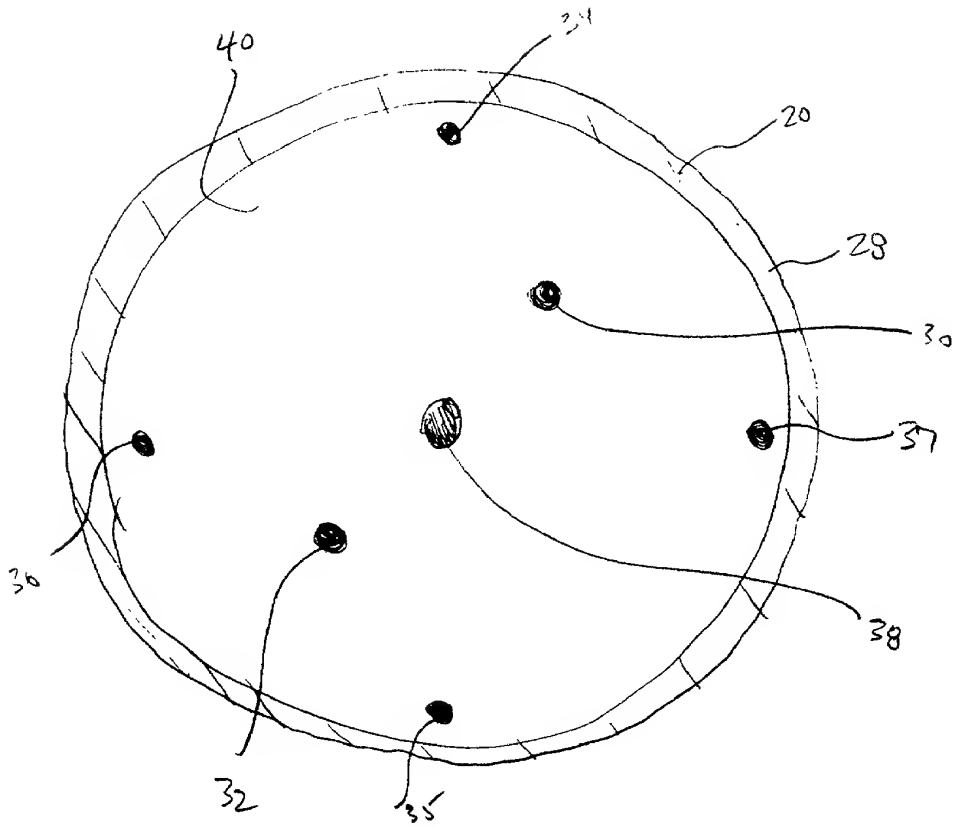
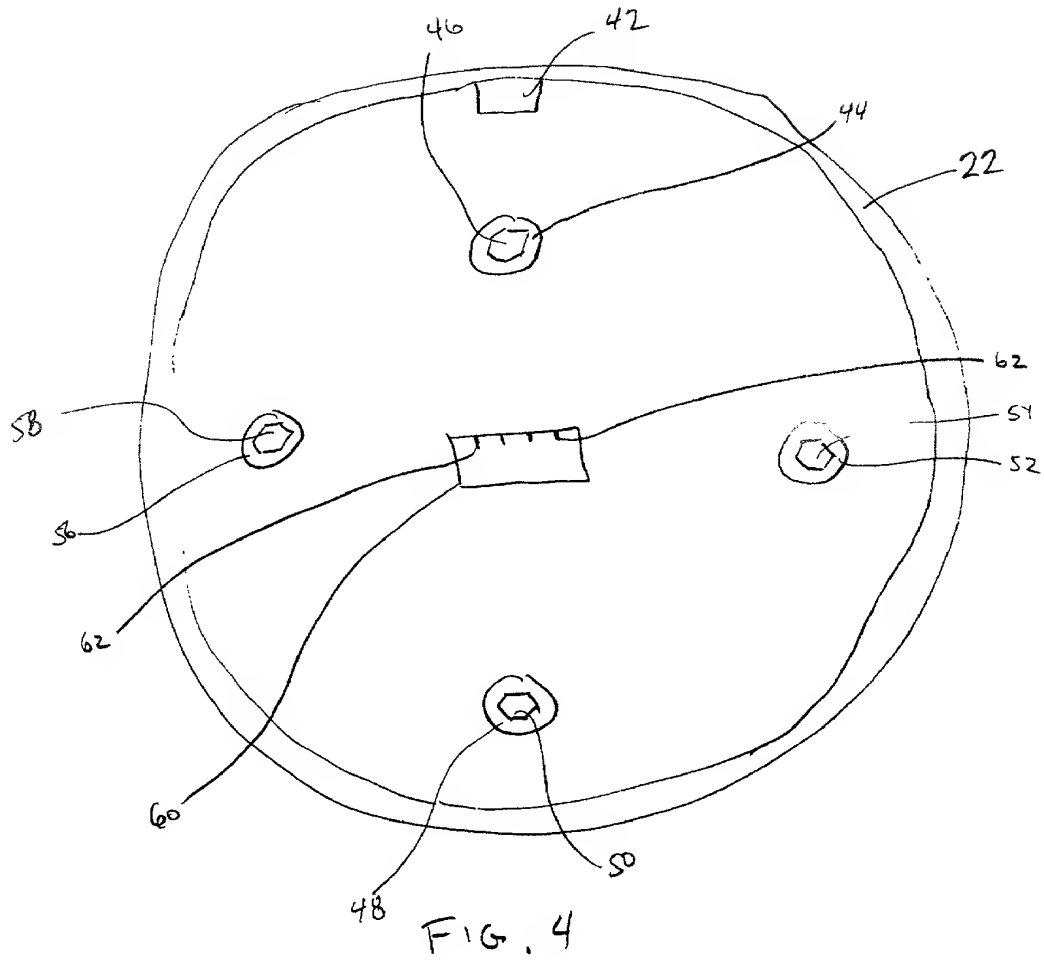


FIG. 3



09236781.041701

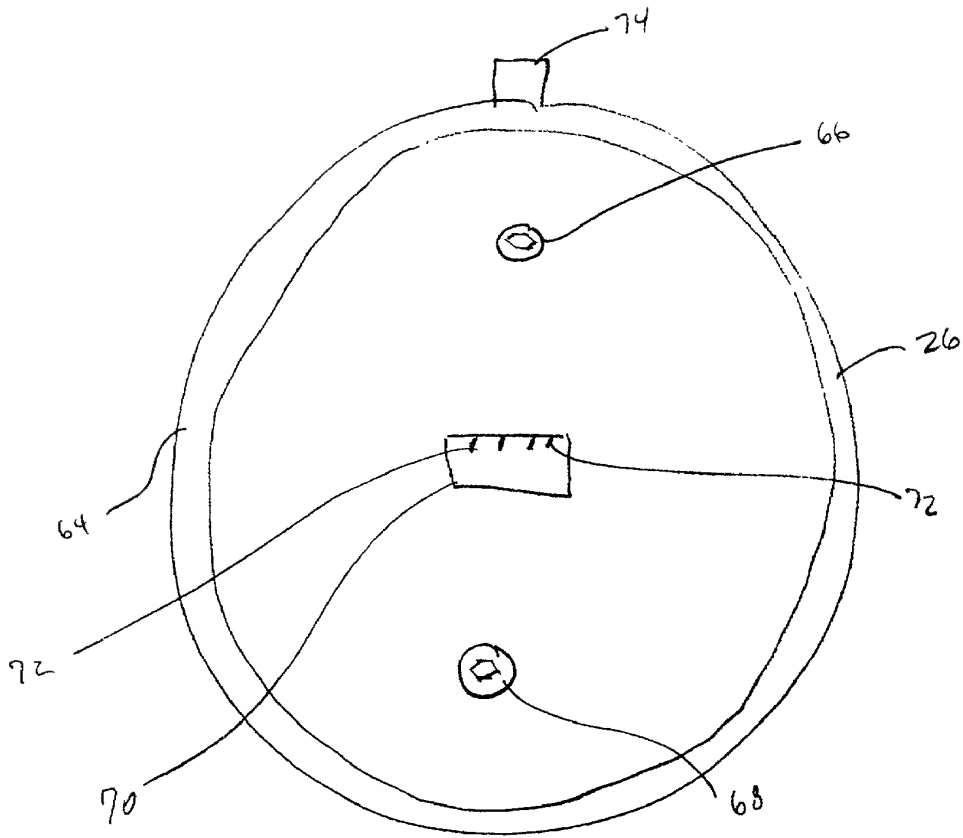


FIG. 5

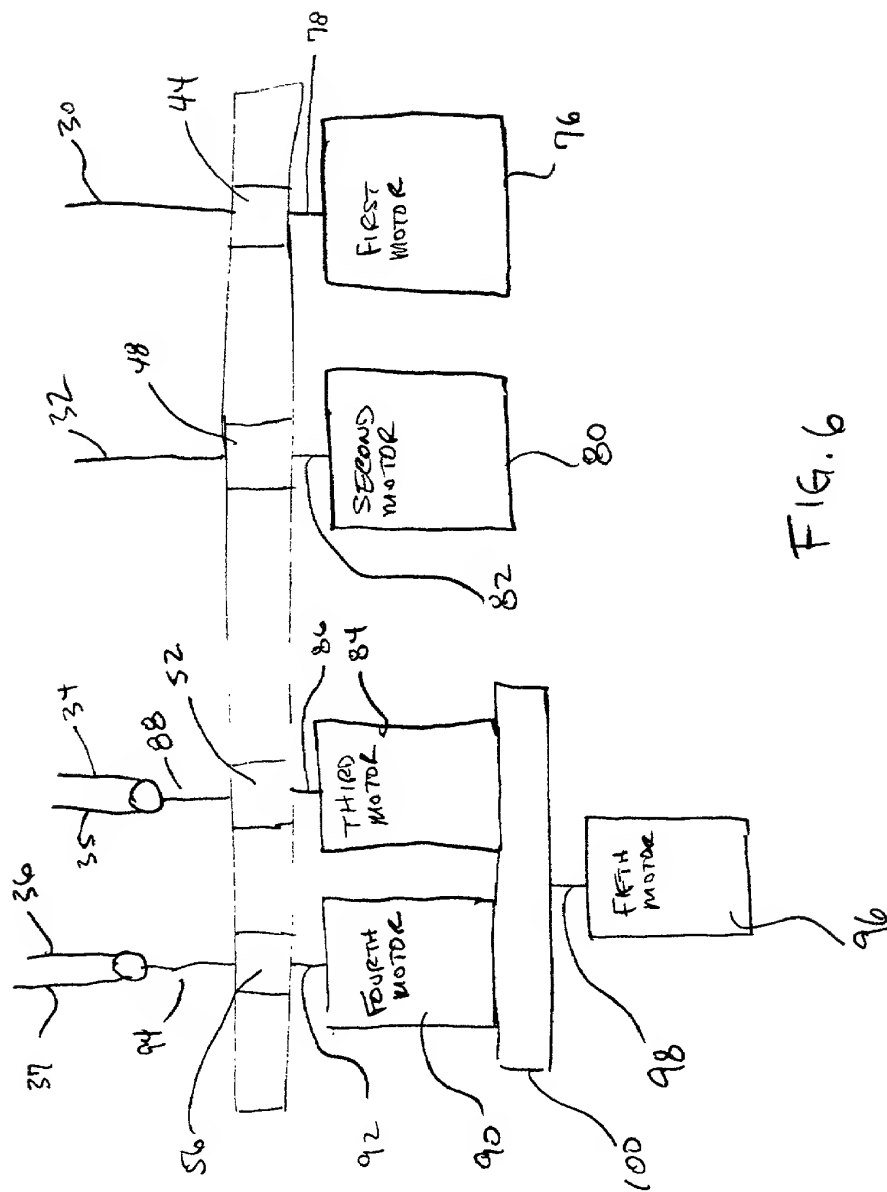


FIG. 6

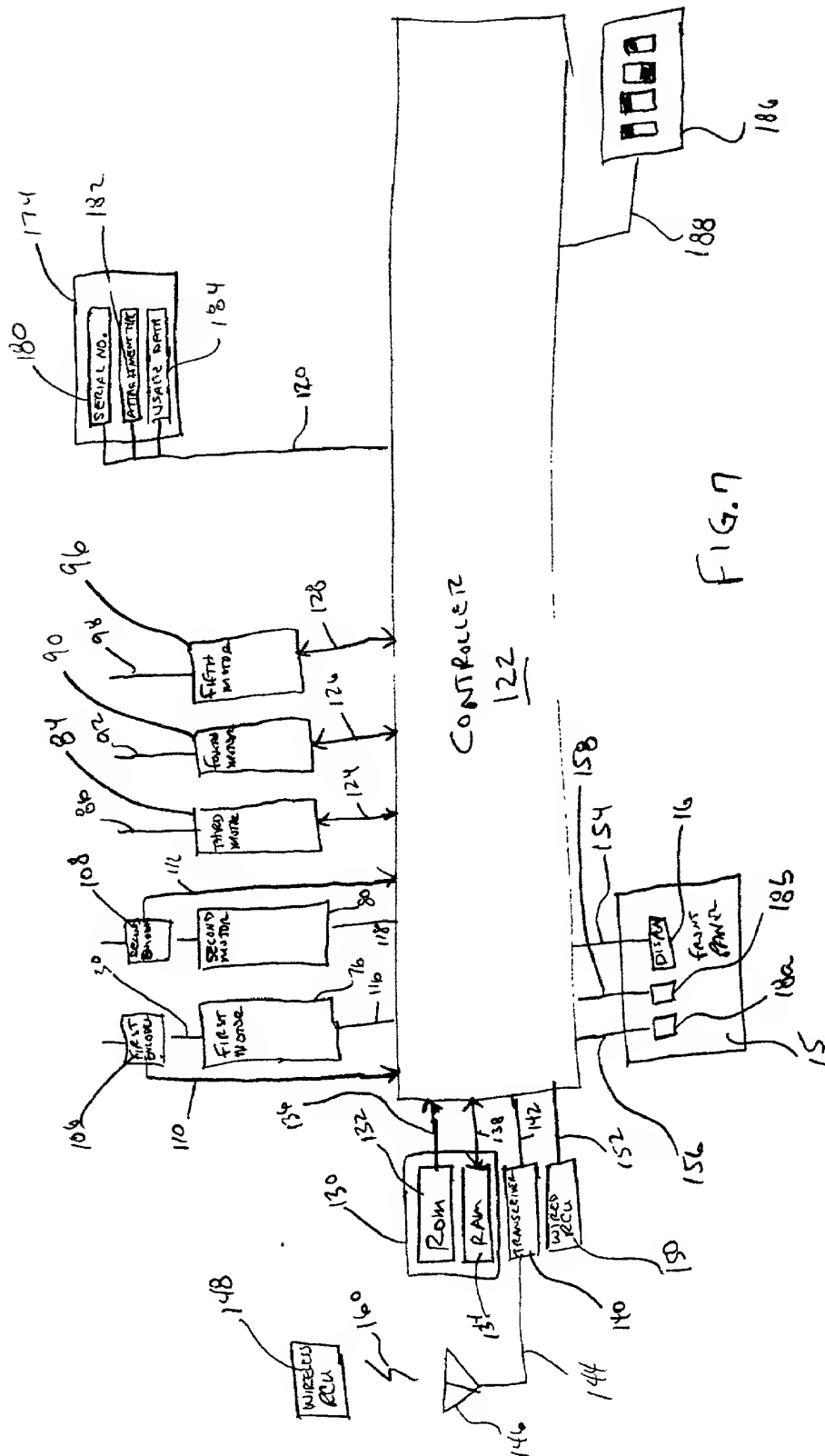


FIG. 7

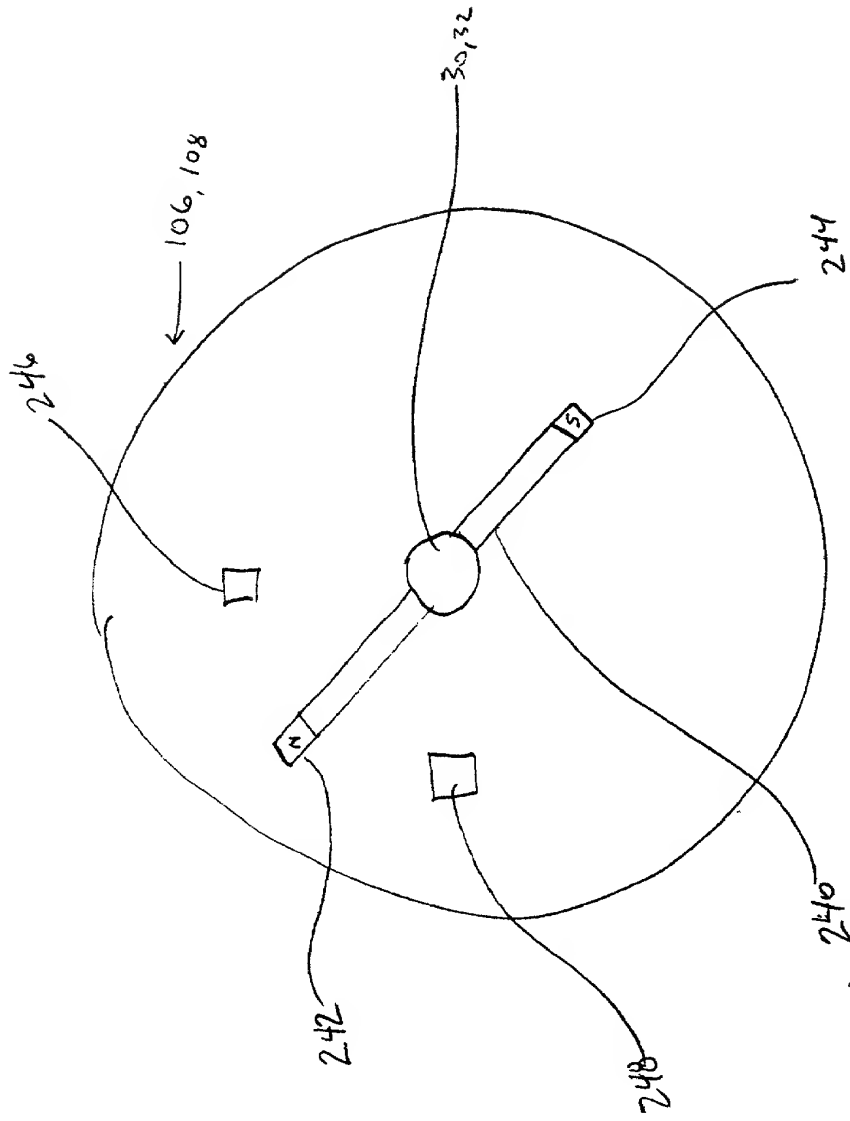


FIG. 8

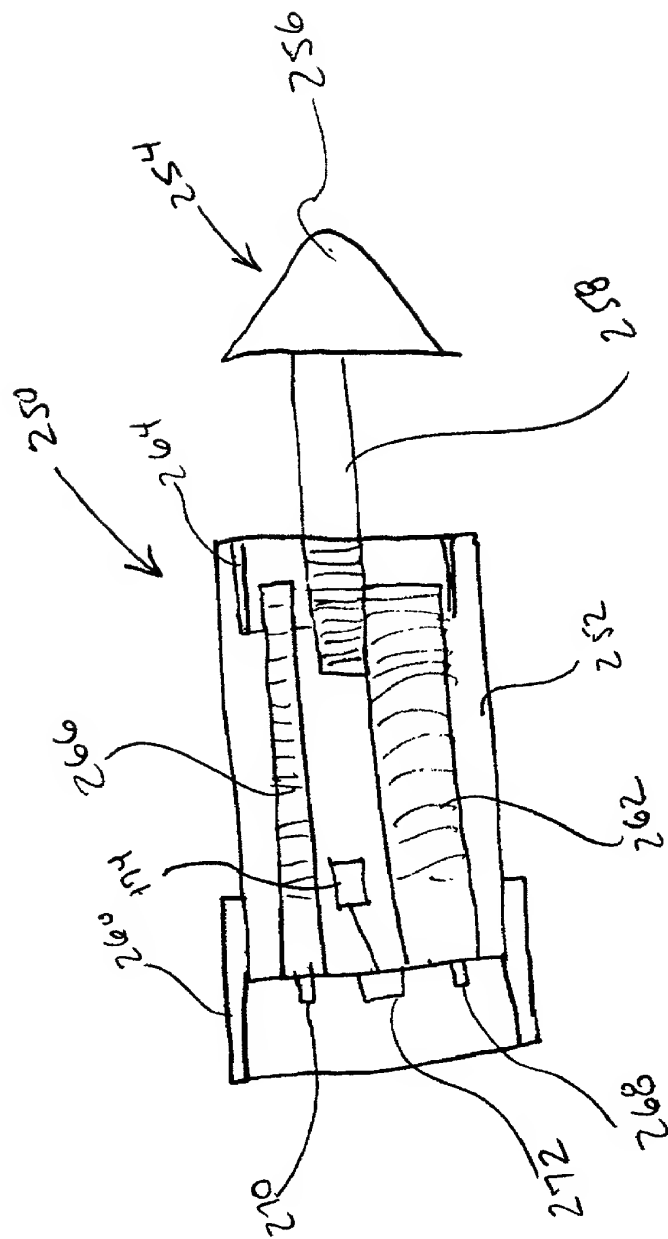


Fig. 9A

09836781, 041701

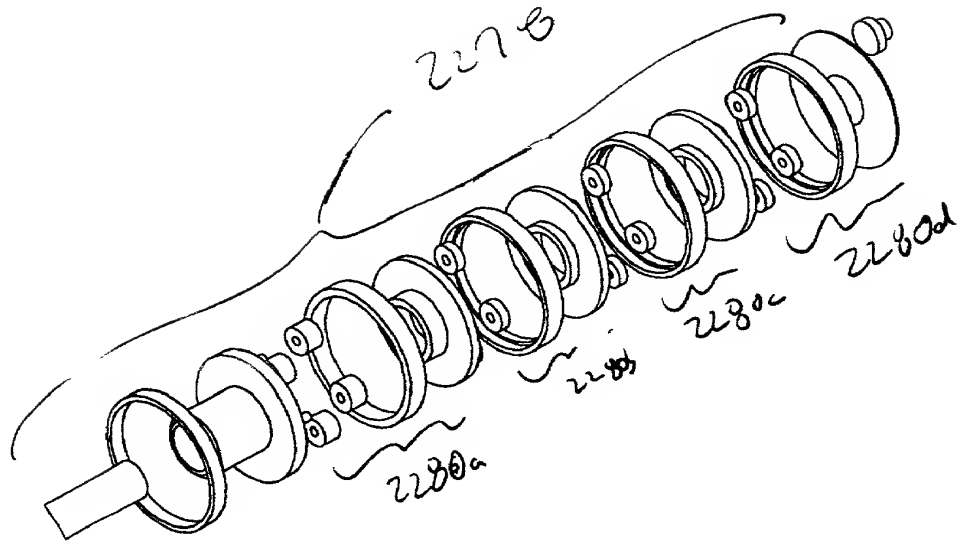


FIG. 9C

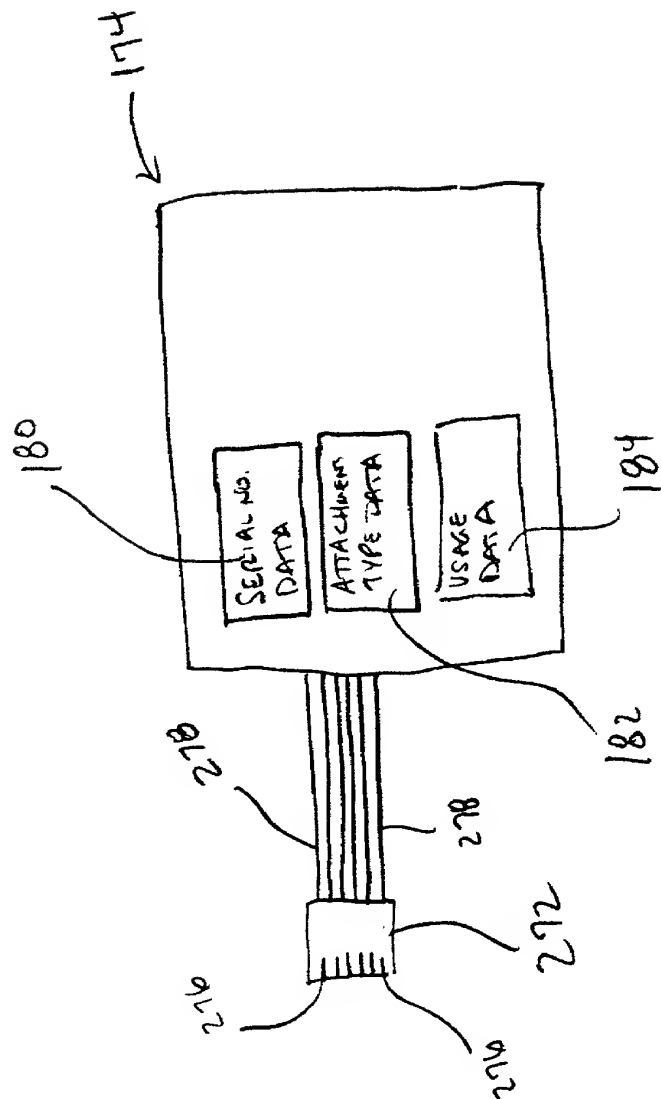
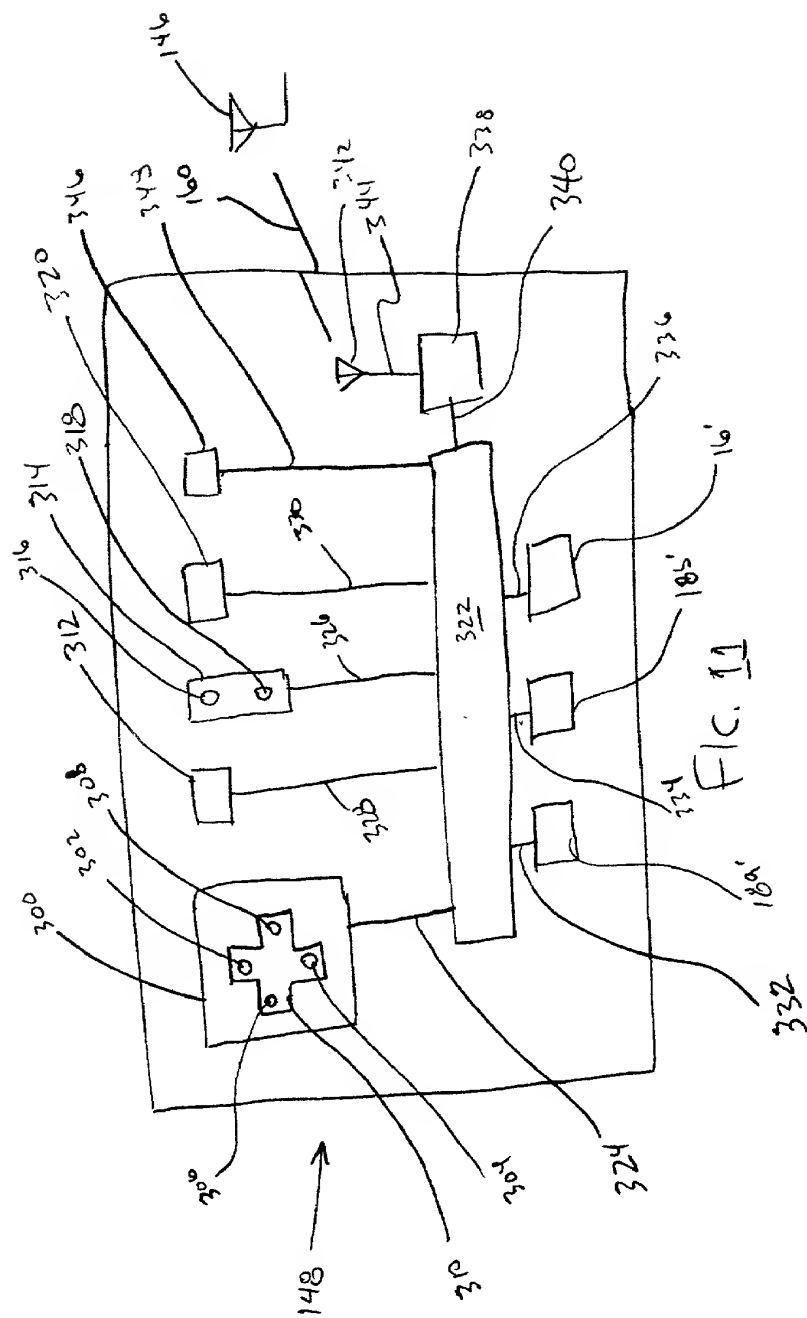
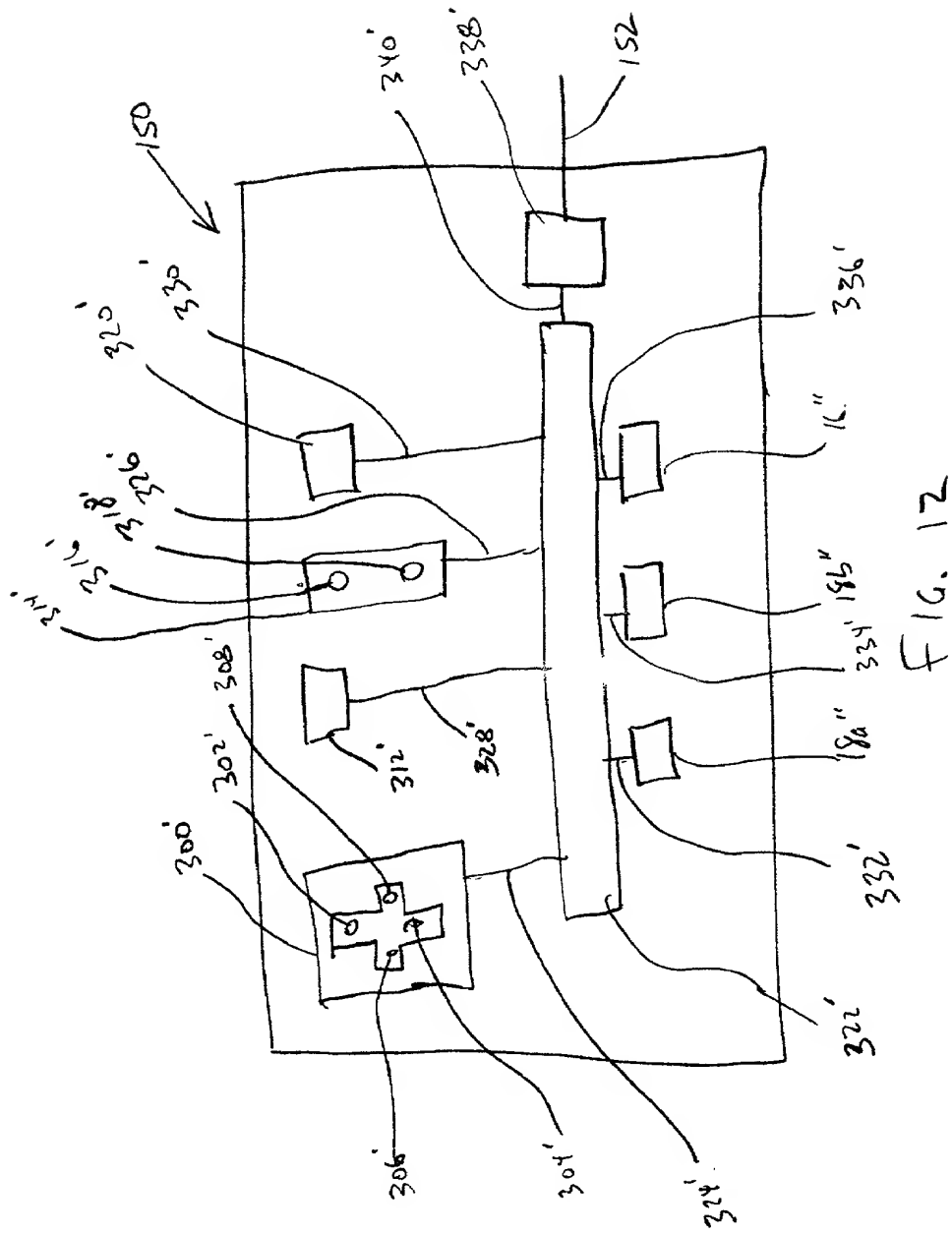


FIG. 10





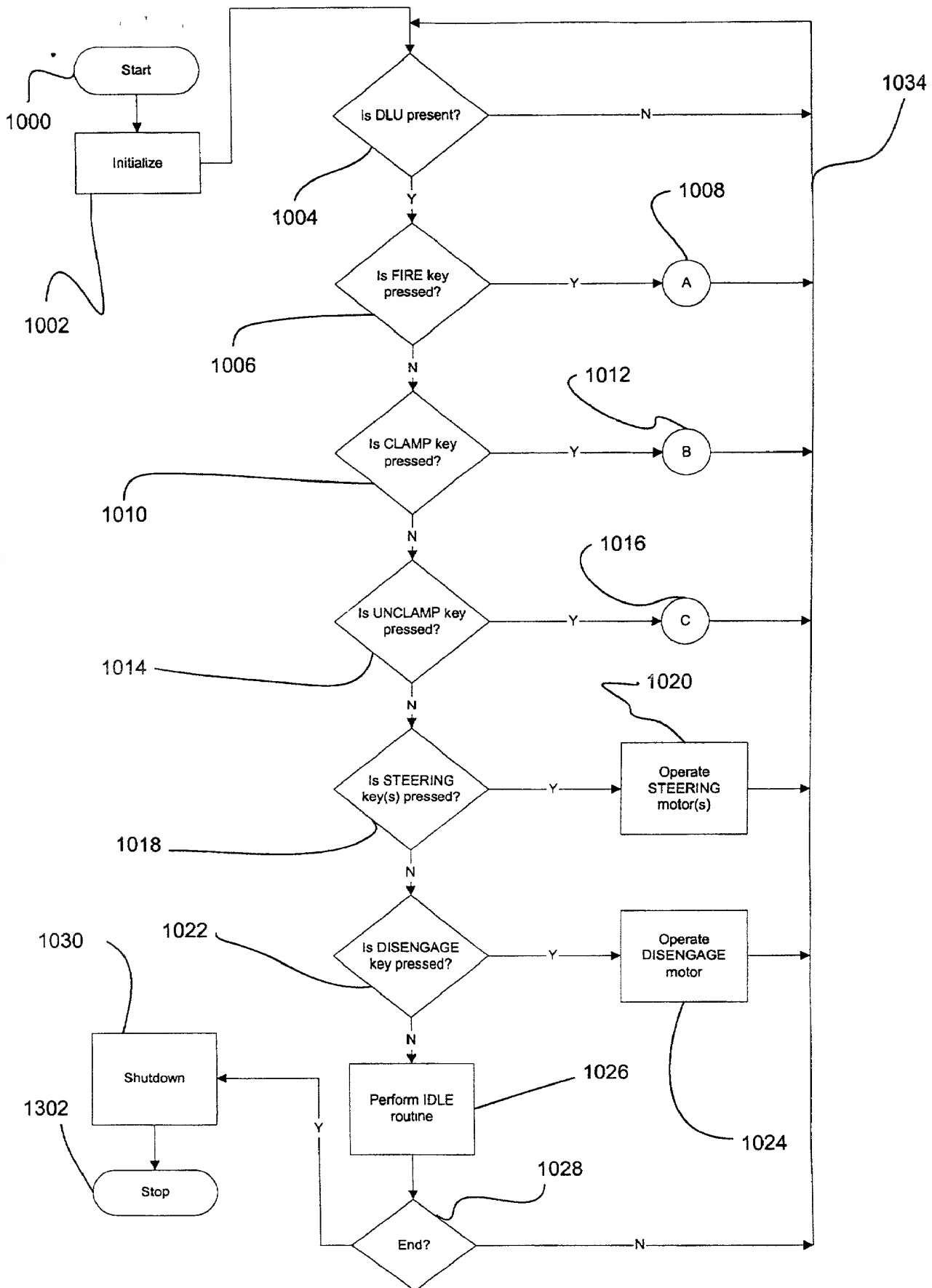


Figure 13

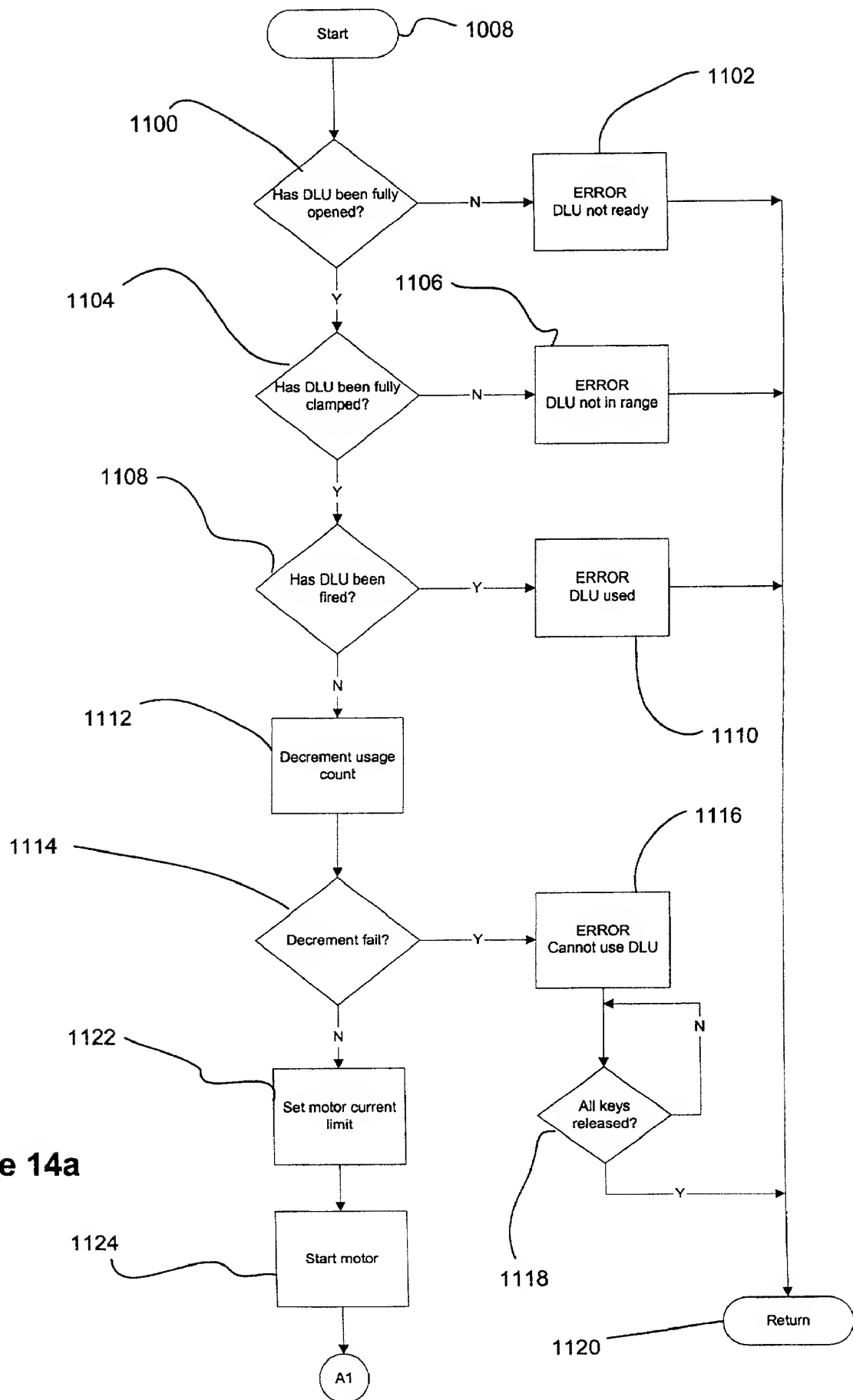


Figure 14a

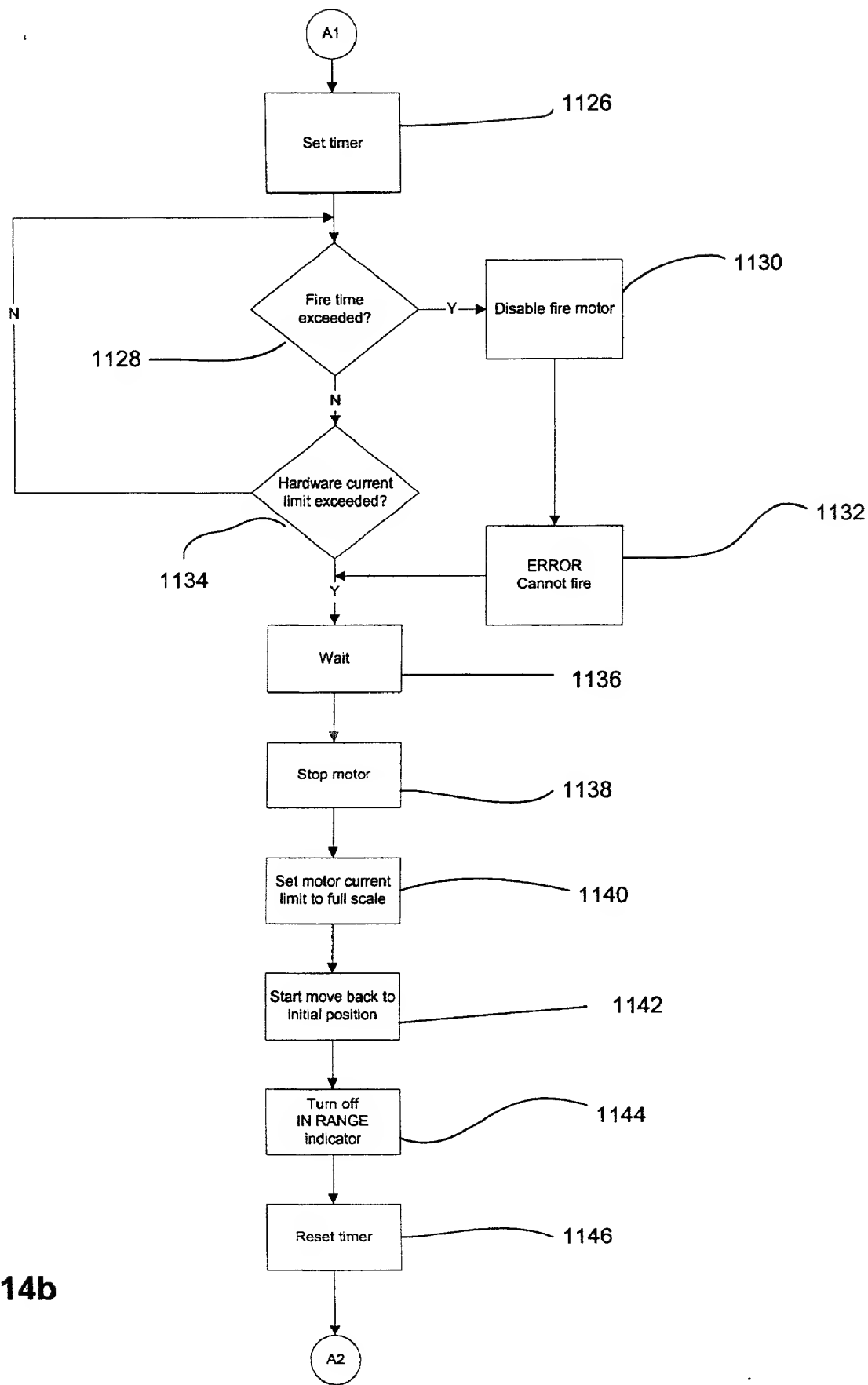


Figure 14b

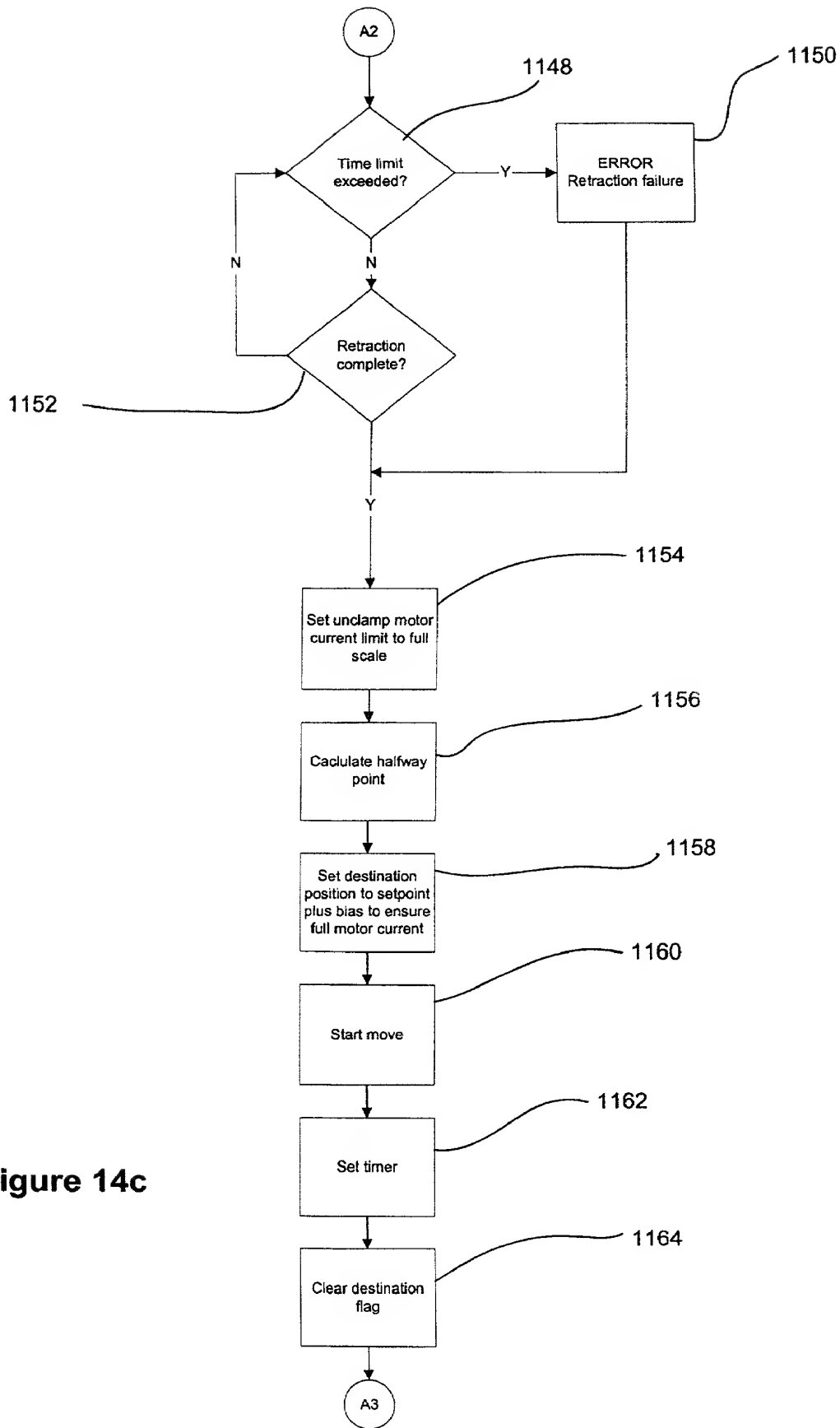


Figure 14c

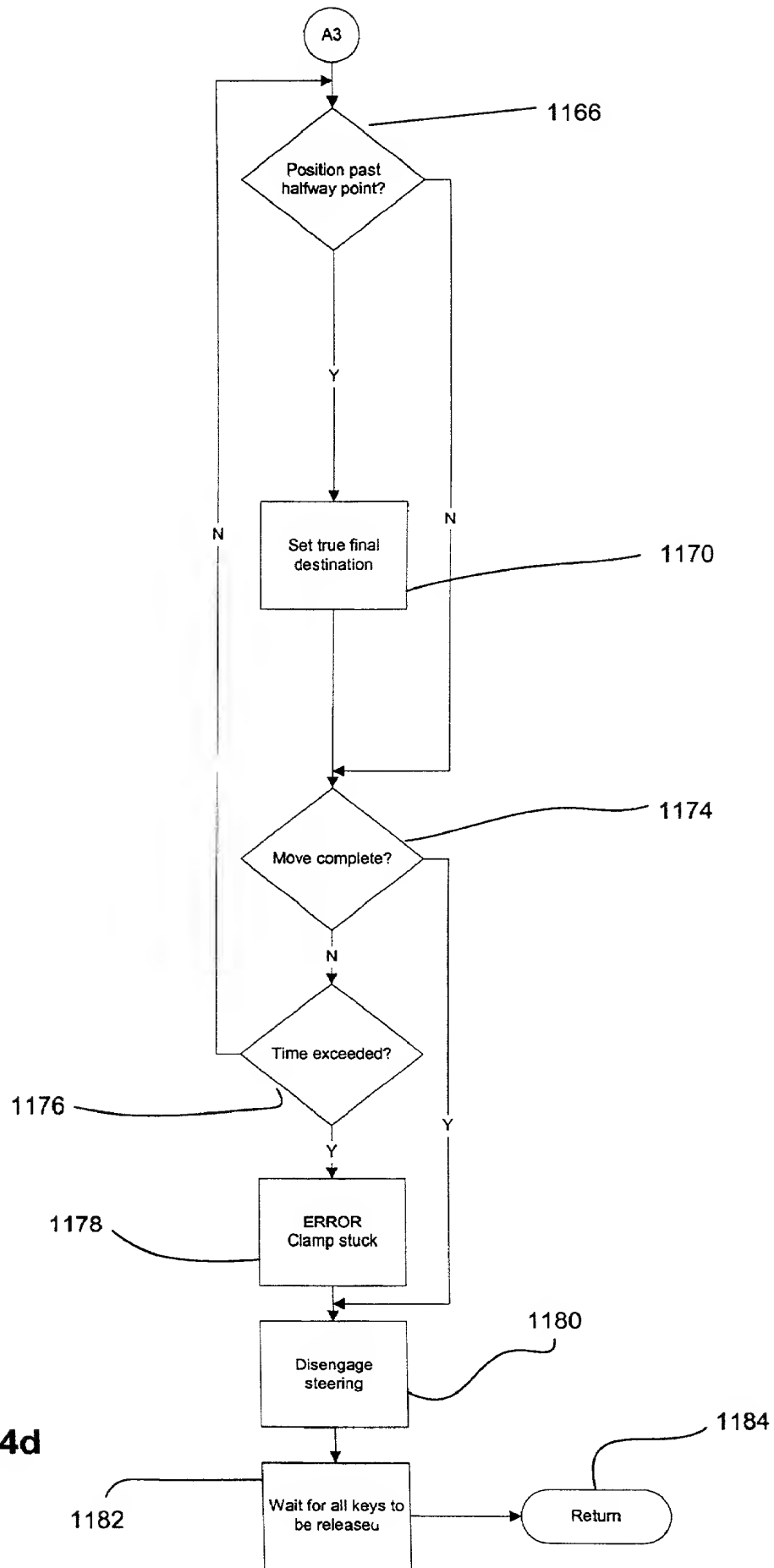


Figure 14d

090317B1 043701
T02T40 T02CEB60

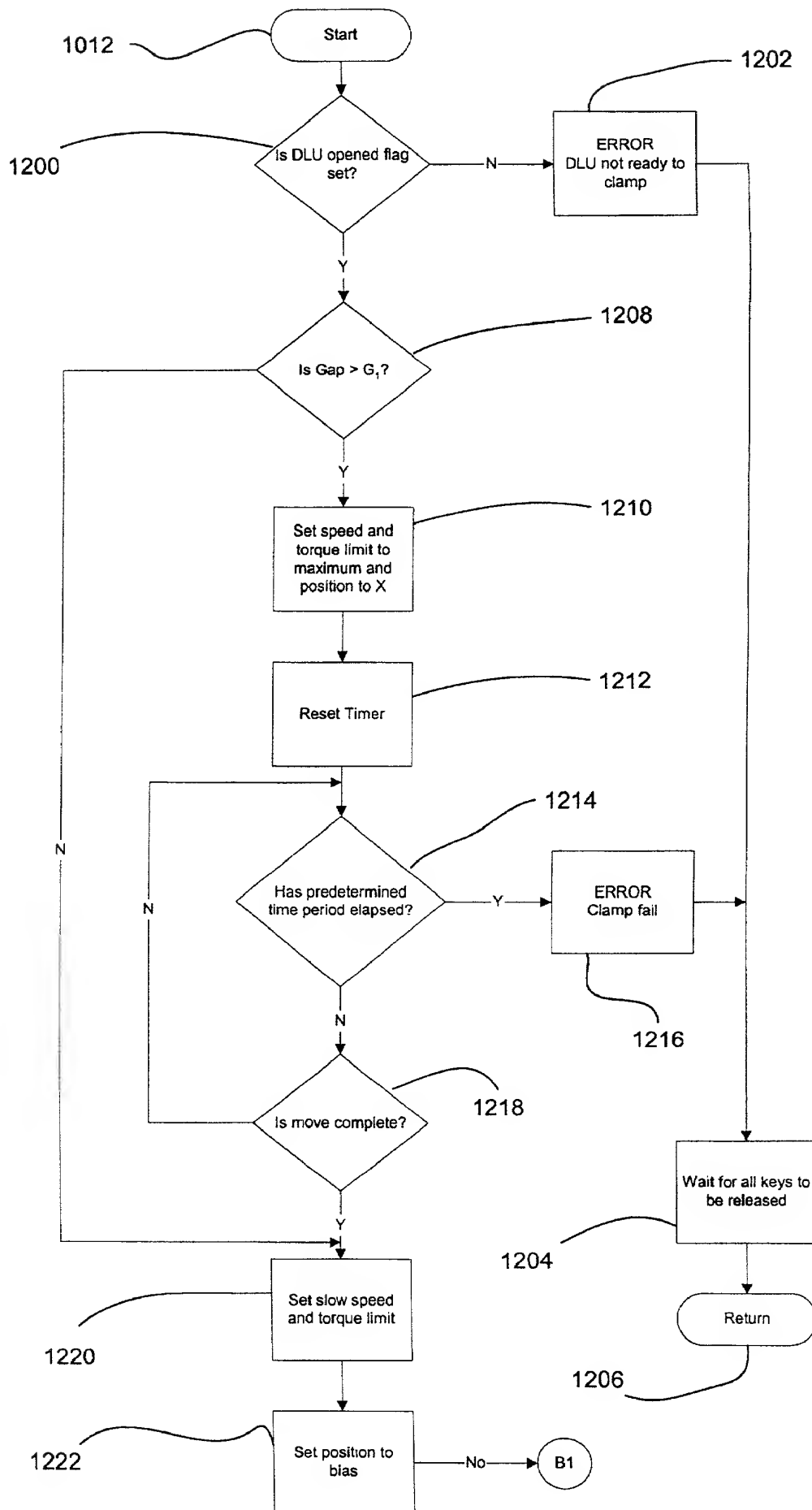


Figure 15a

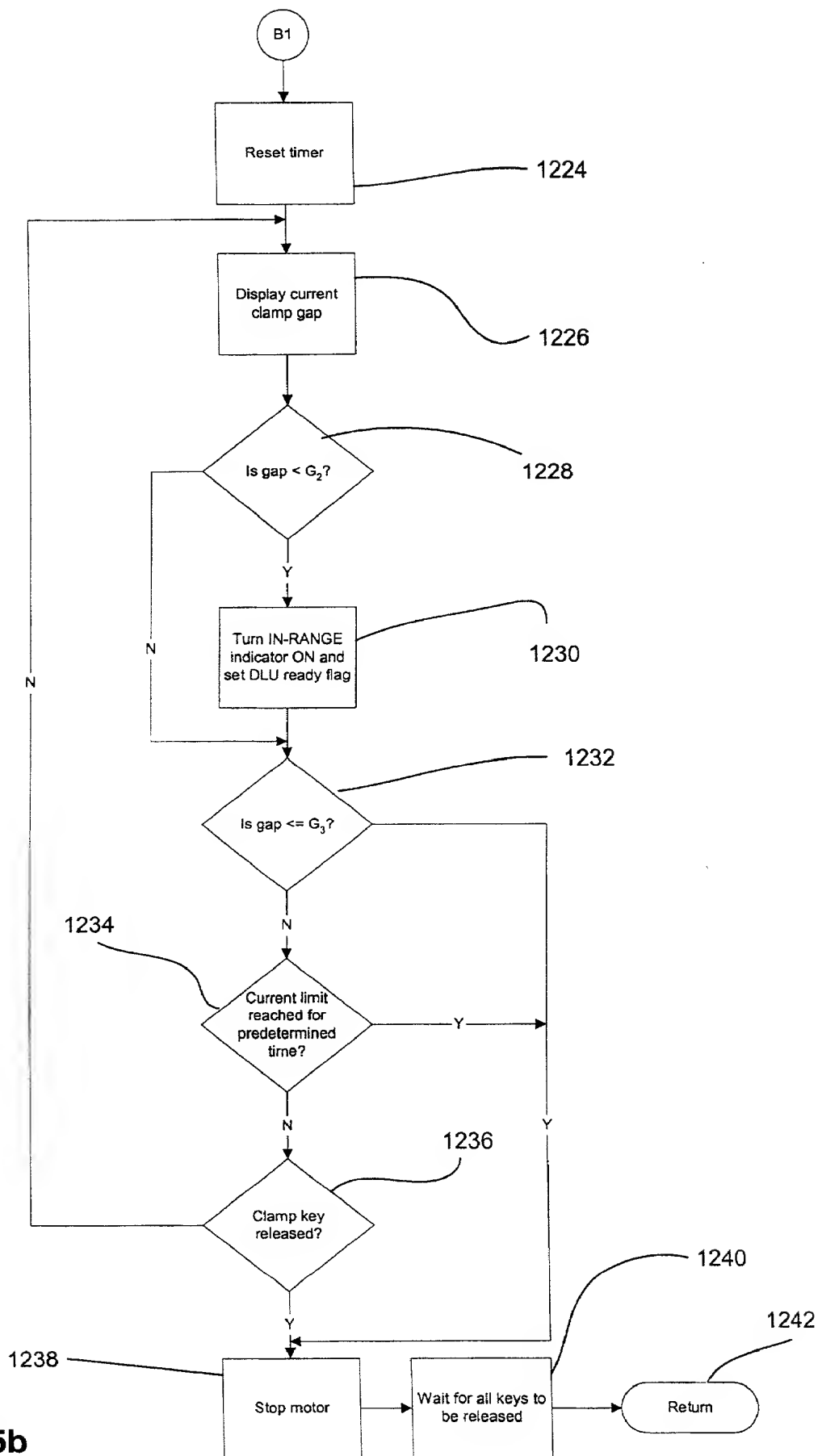


Figure 15b

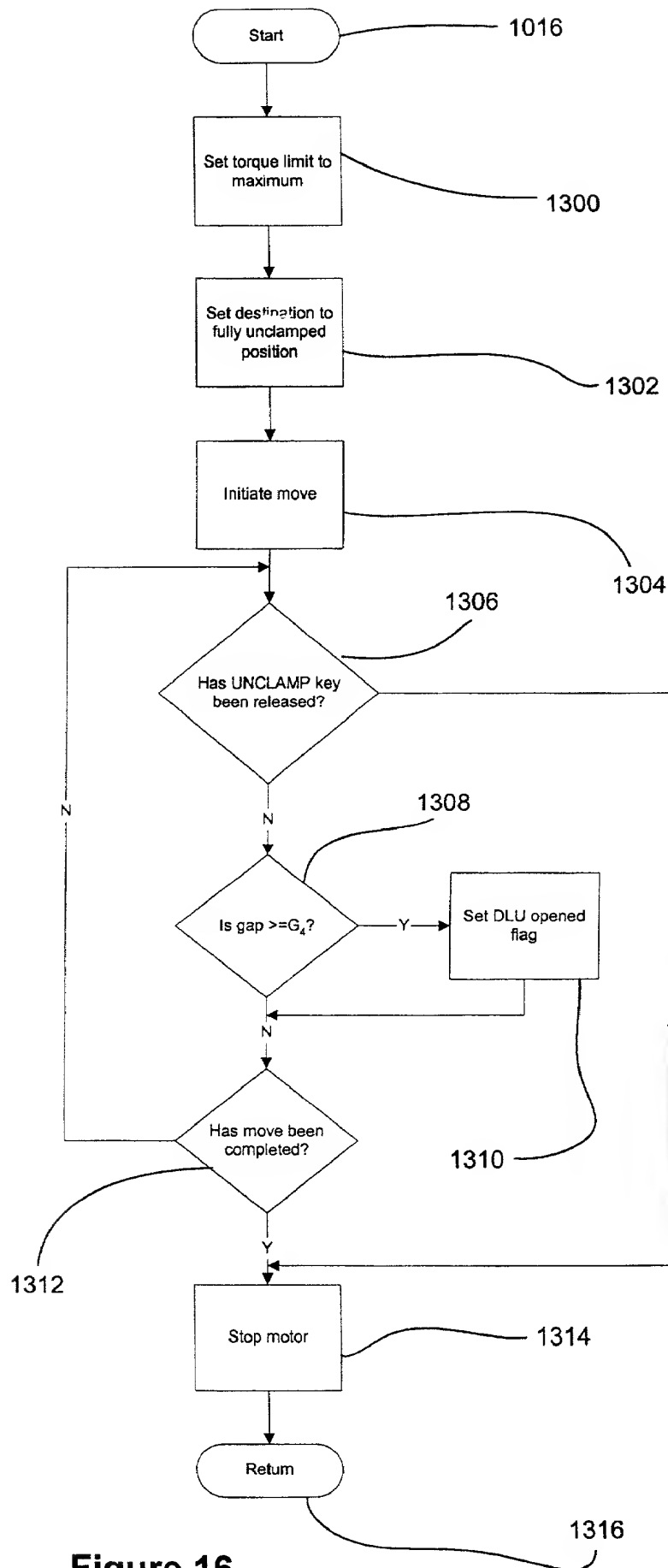


Figure 16

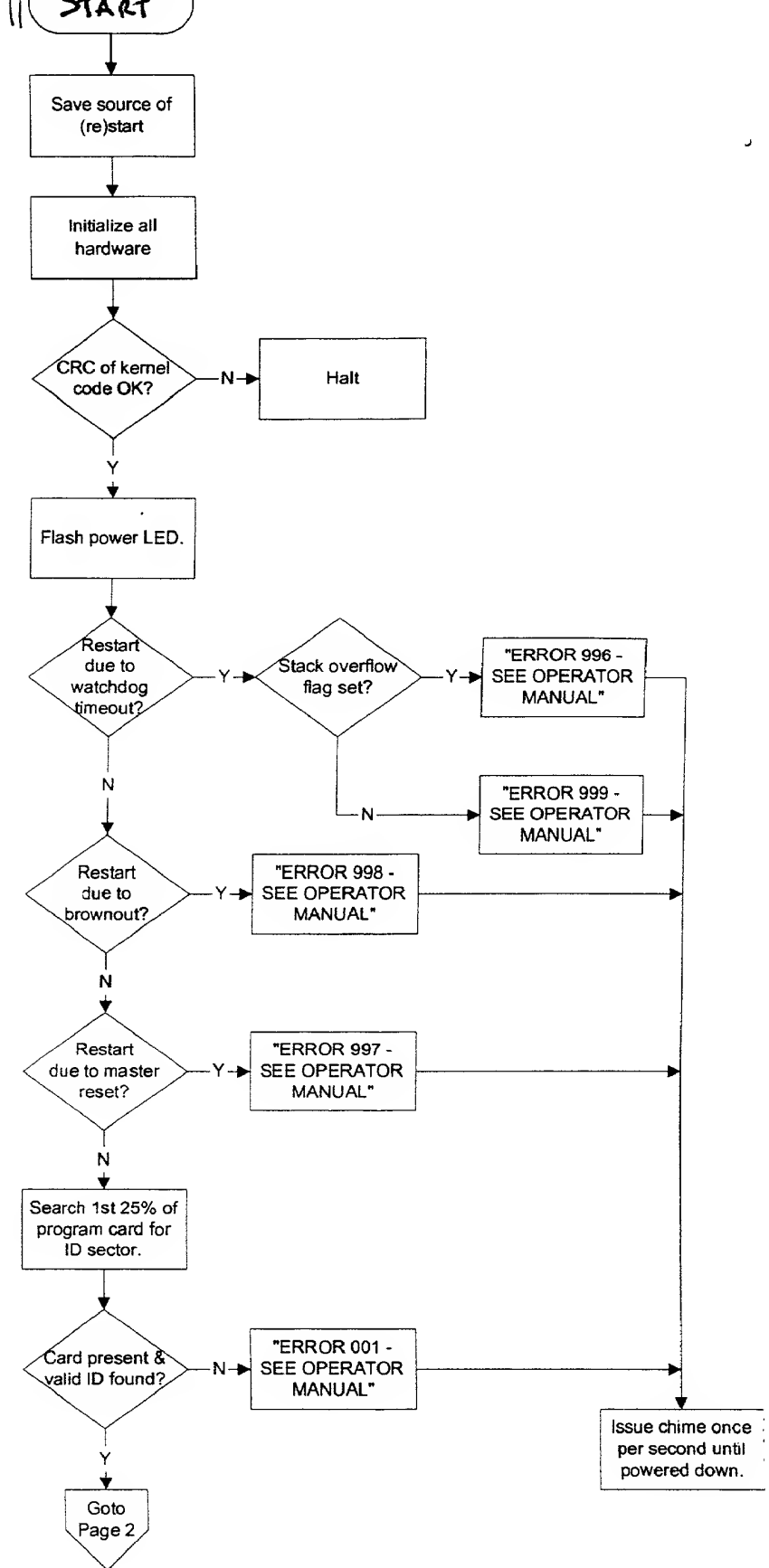


FIG. 17a

09036781.041704

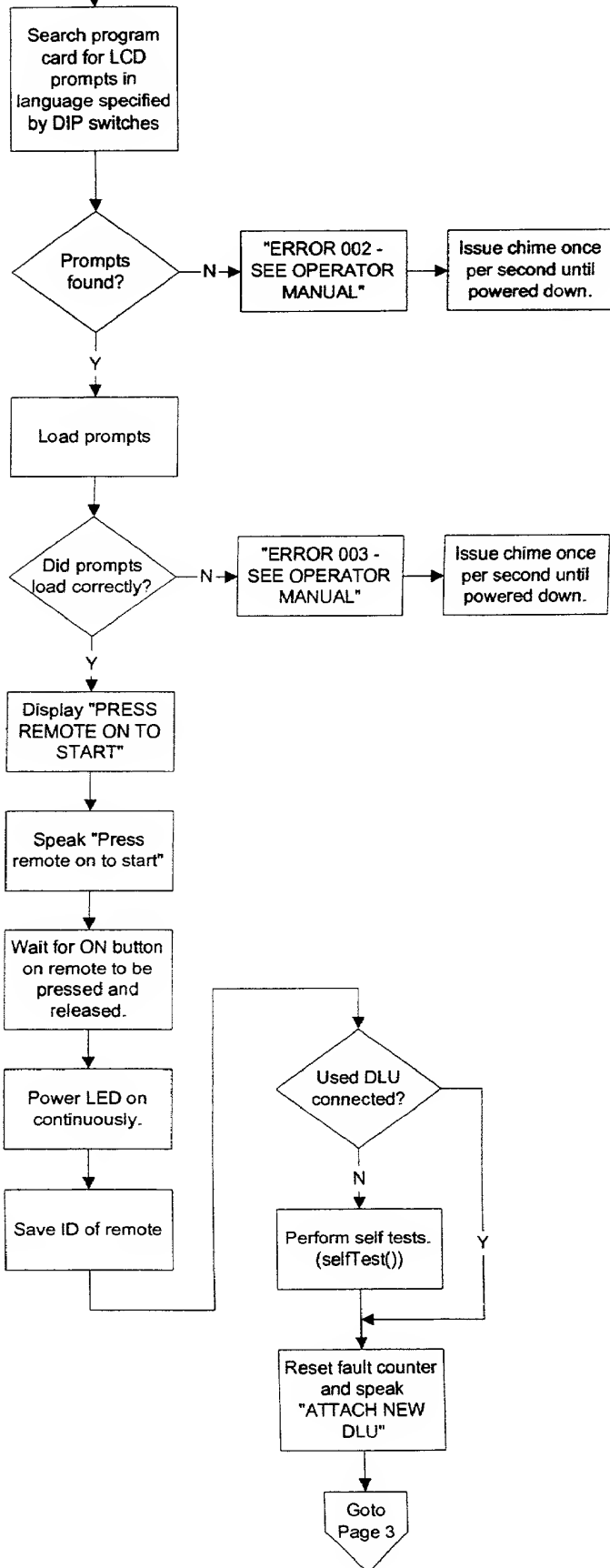


FIG. 17b

09333781-041701
10/14/01 12:23:55

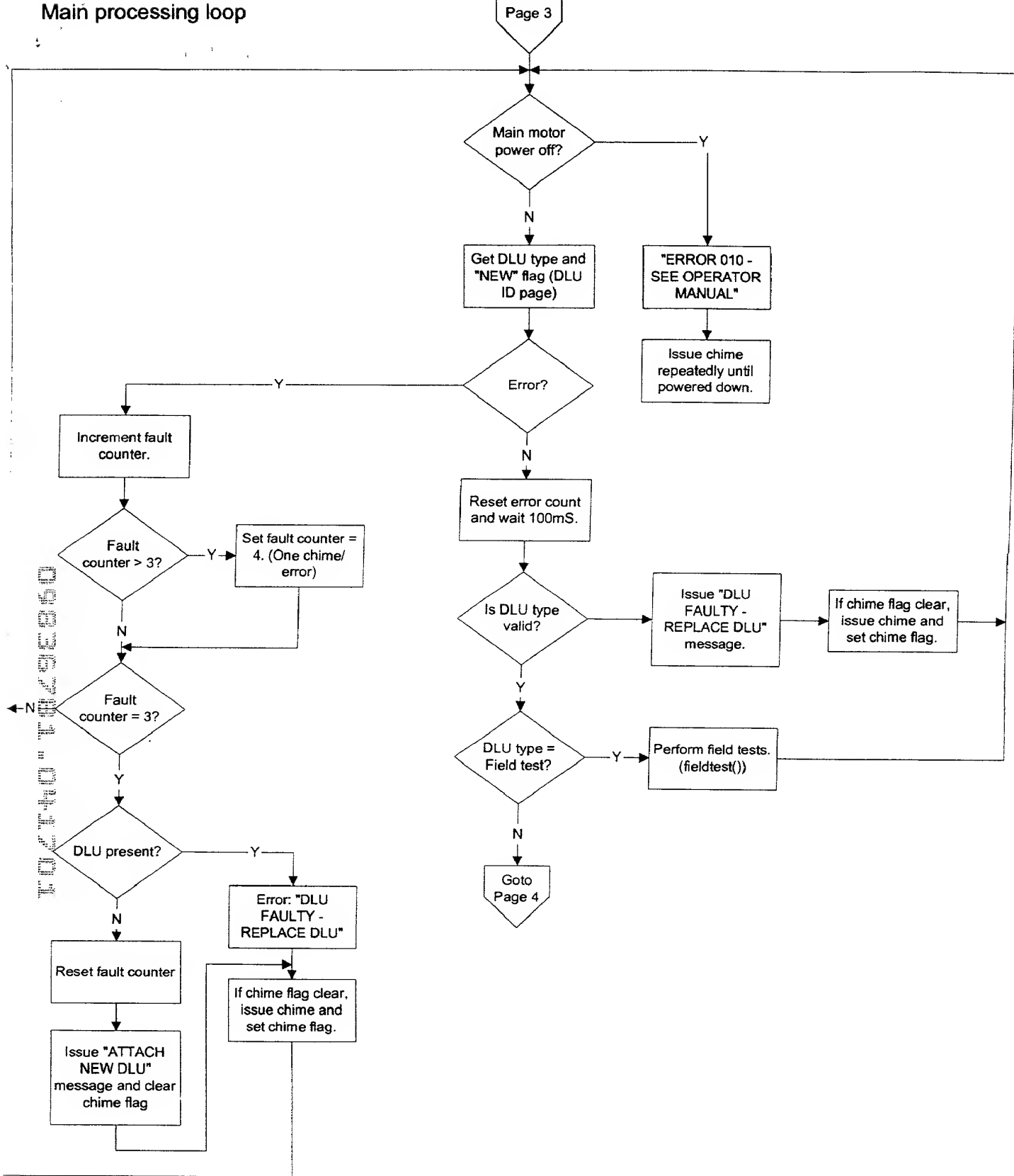


FIG. 17c

030367B1-044701
T04T40 T249350

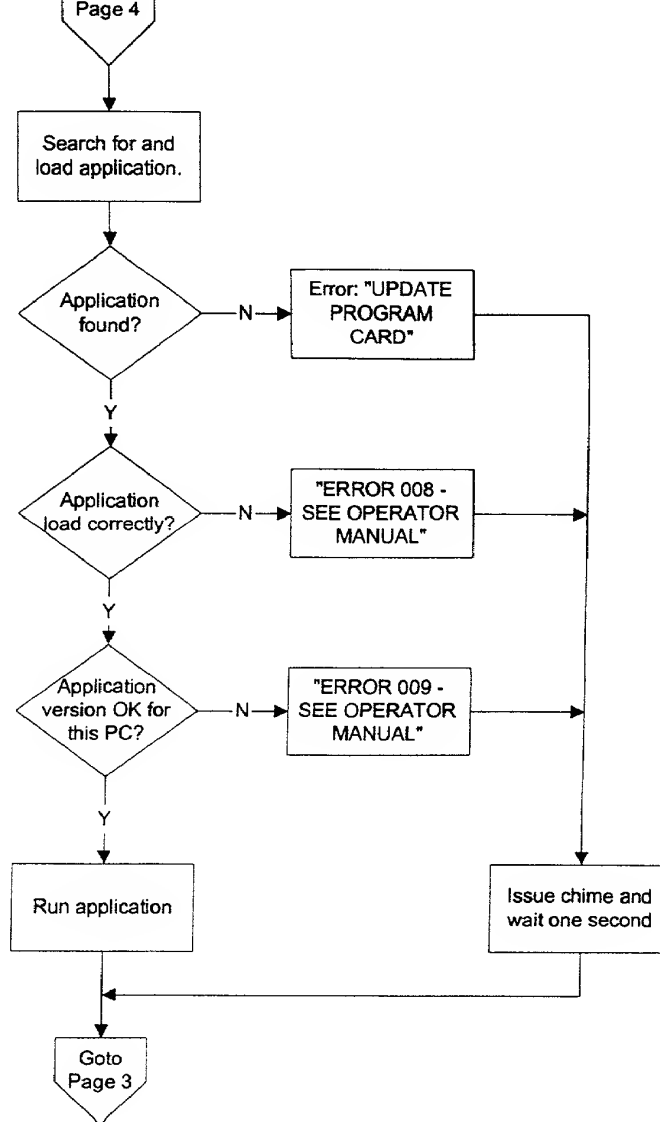


FIG. 17d

0936701 04701
T0470 T036660

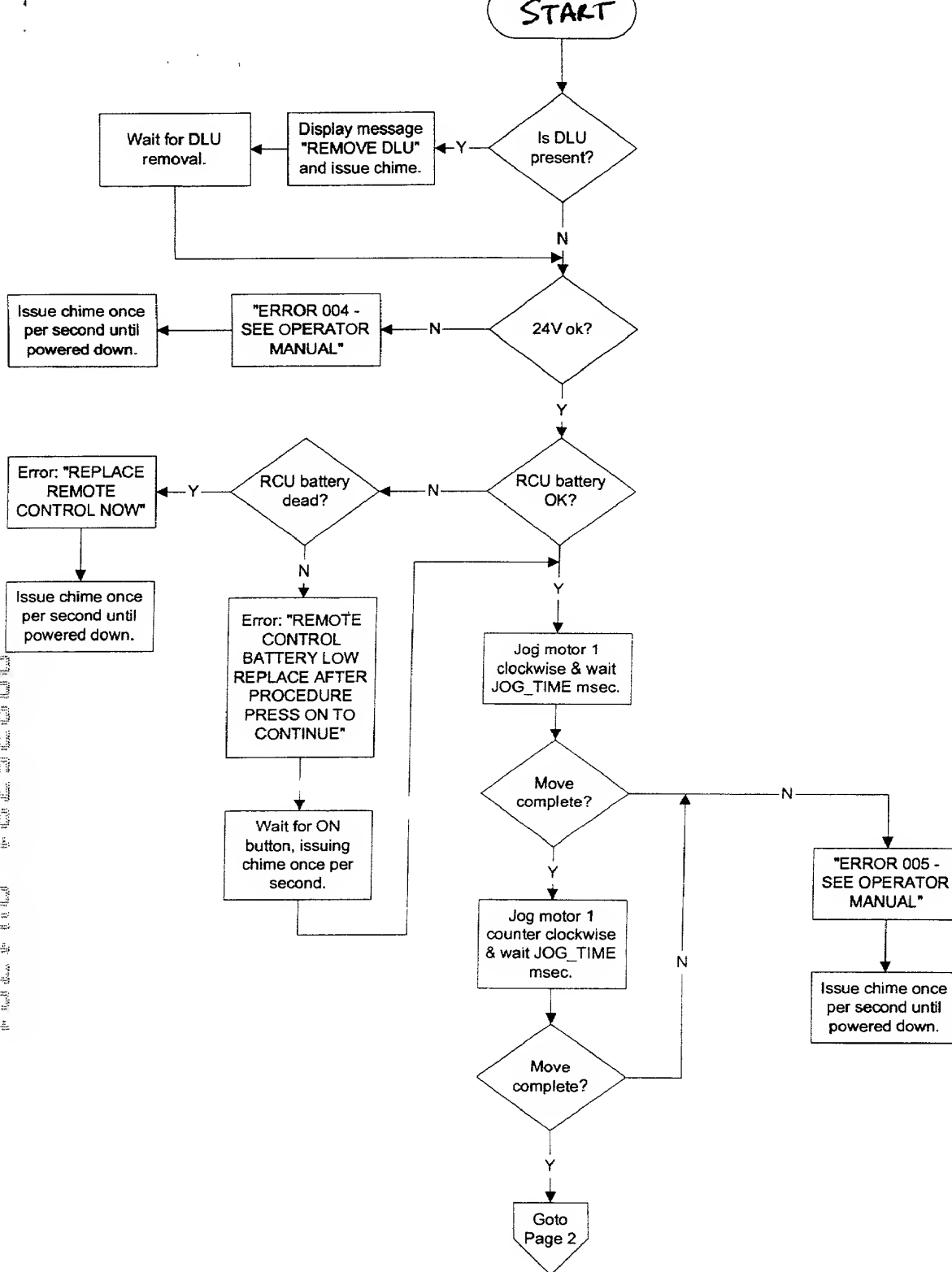
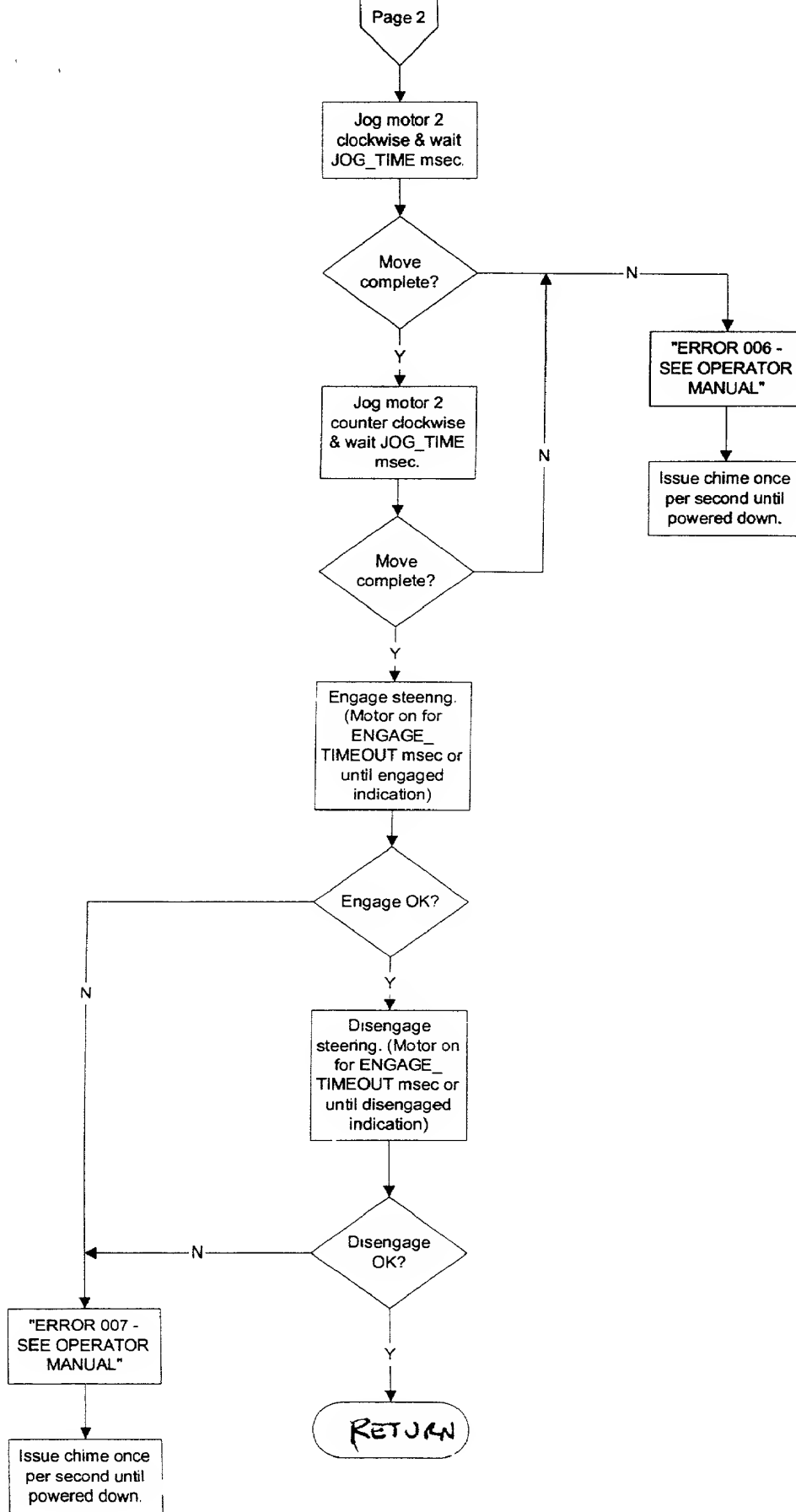


FIG. 18a

09036781.04.1701



09030701.041701

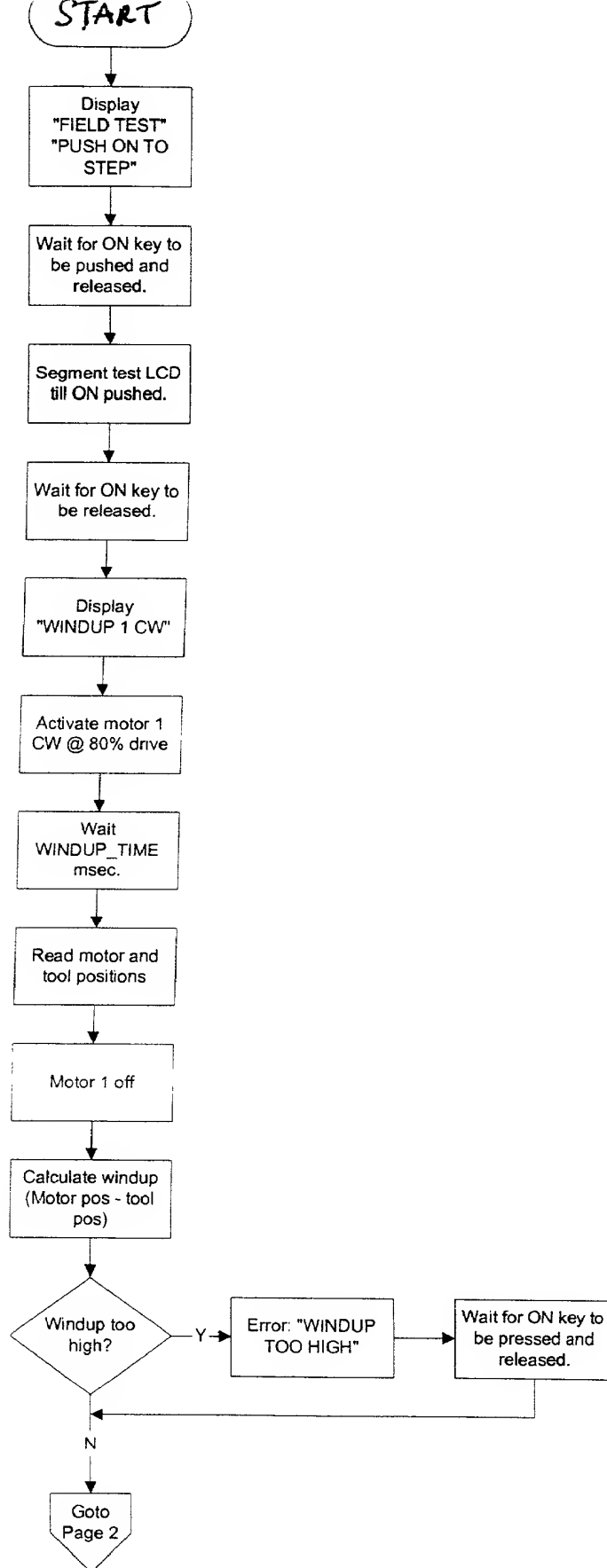


FIG. 19a

09236781.043701

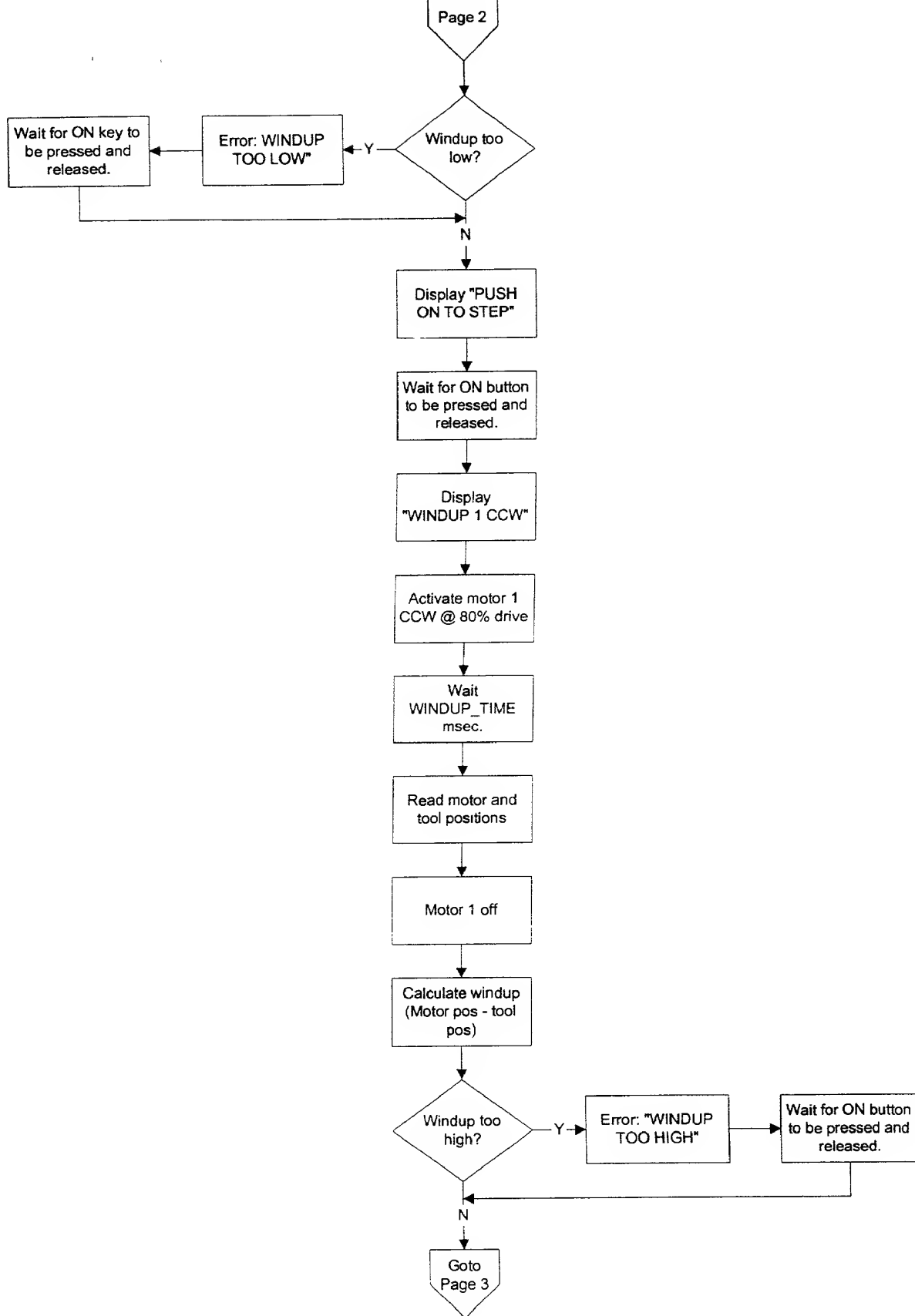


FIG. 196

098367034 041704

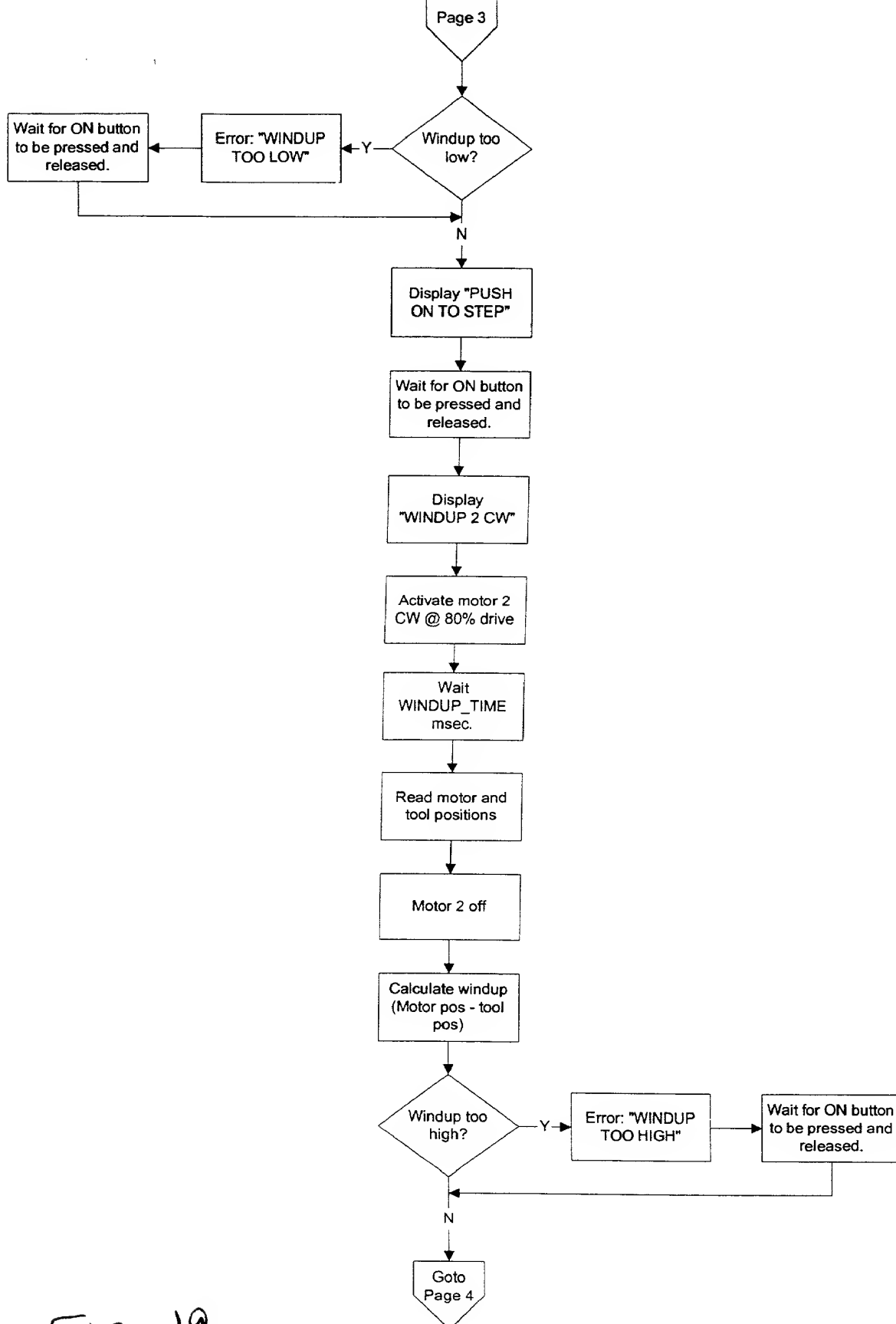
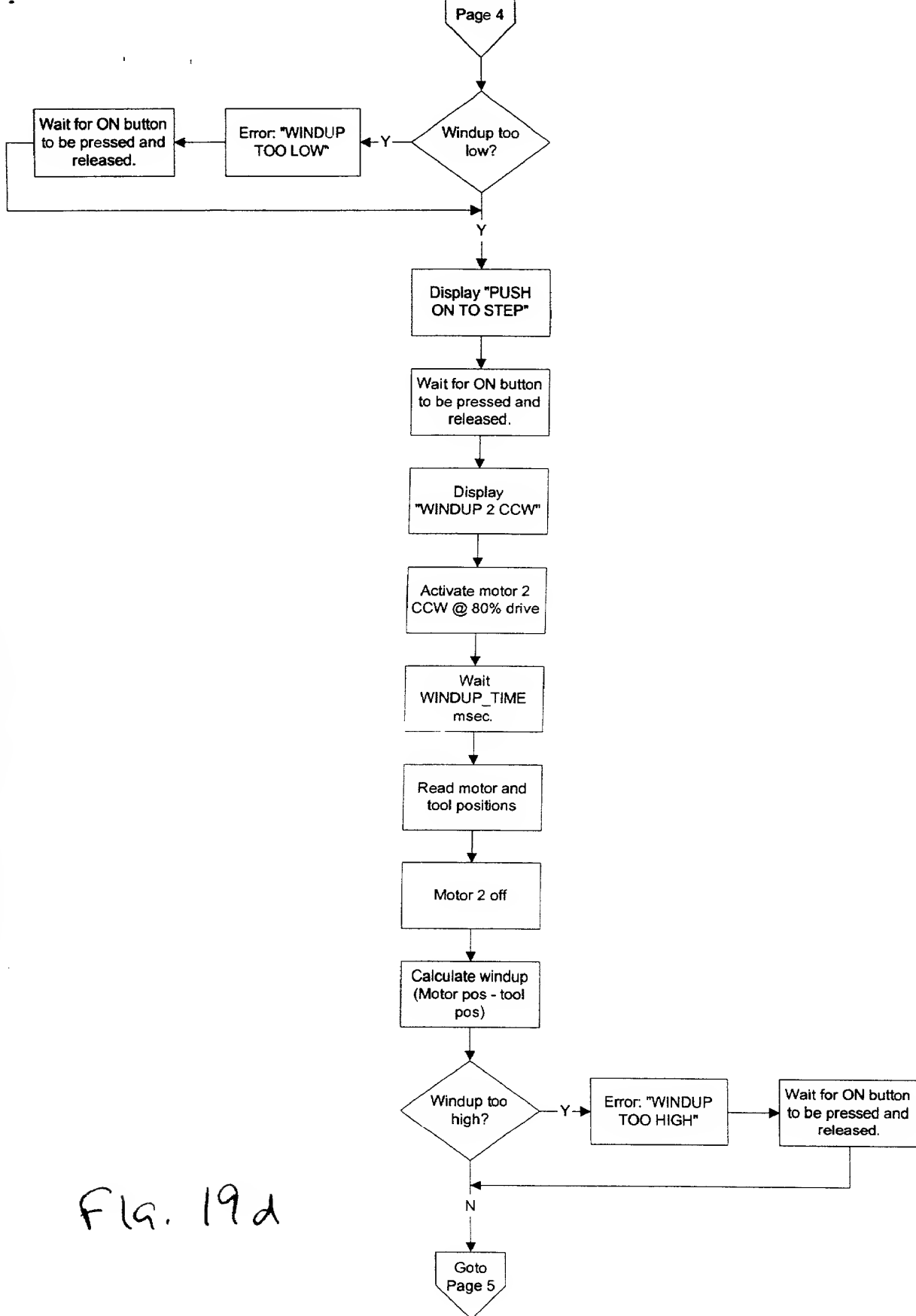


FIG. 19c

0936791-04-104



09836781.041701

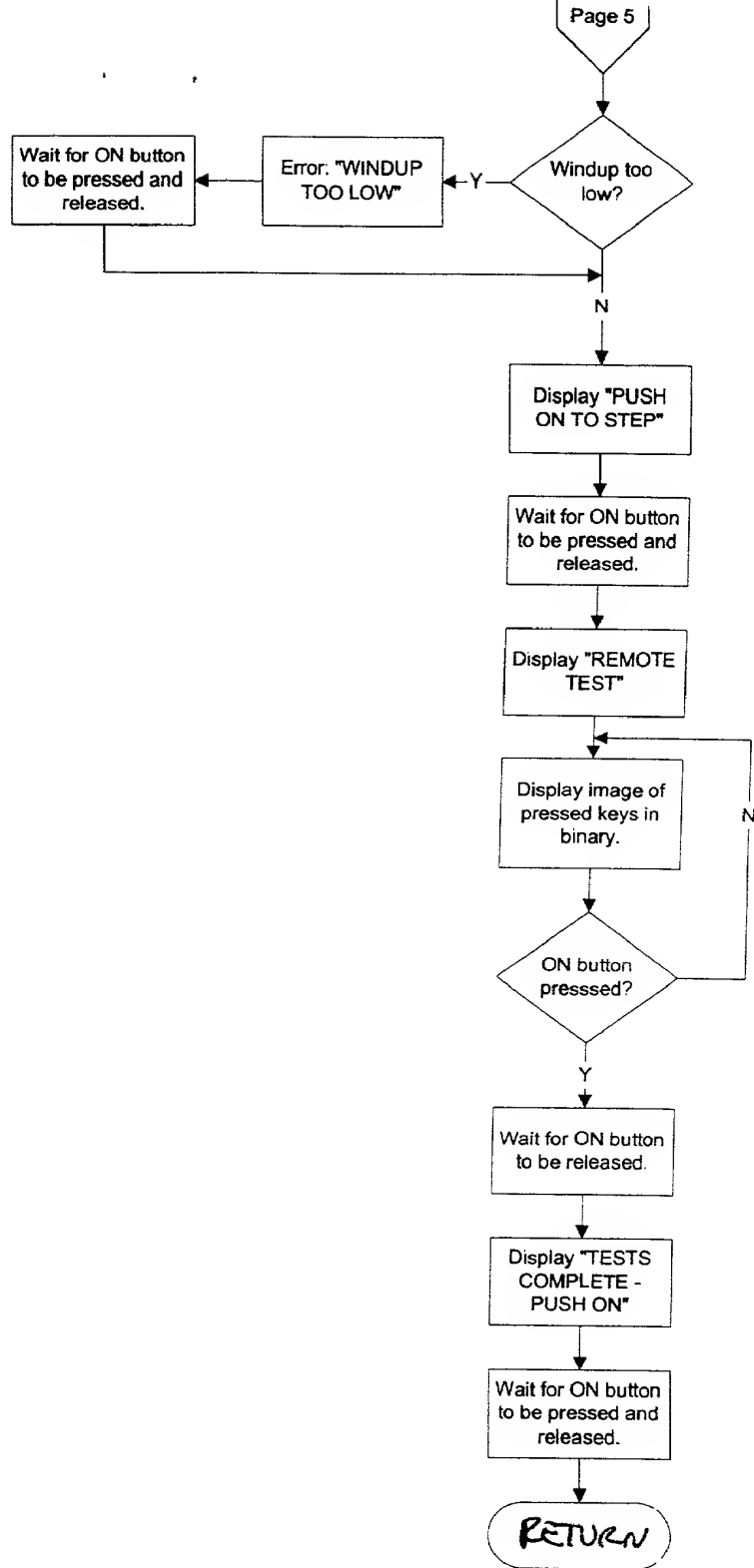


FIG. 19e

START

Initialize motor/tool
end positions:
1mm for clamp,
0mm for knife.

Clear DLU
opened, used,
present, old, and
ready flags.

*Kernel tries 3x w/ 100mS
between tries to read the DLU
EPROM.

Read DLU new
flag from EPROM.

Read OK?

*Kernel tries 3x w/ 100mS
between tries to read the serial
number of the DLU.

Read and save
serial number of
DLU.

Read serial
number OK?

Error: "DLU
FAULTY-
REPLACE"

Issue audible
chime and wait 1
second.

Return to kernel.

Display message
"ATTACH NEW
DLU"

Set old DLU flag,
fast blink rate,
issue chime.

Is DLU new?

Set DLU used flag
to suppress self
test on open.

Display and speak
"READY"

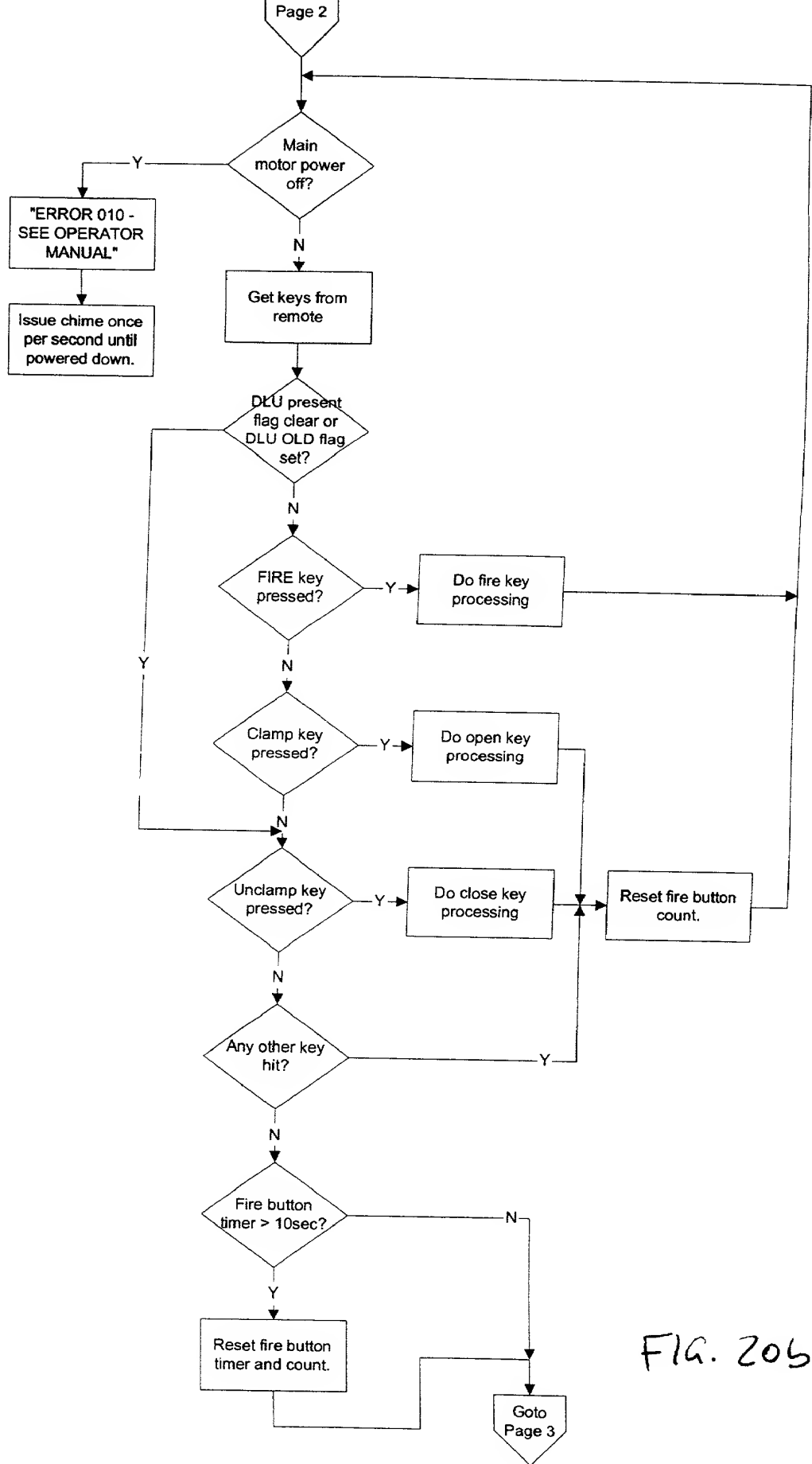
Reset timer, fire
button timer, and
fire button count.

Goto
Page 2

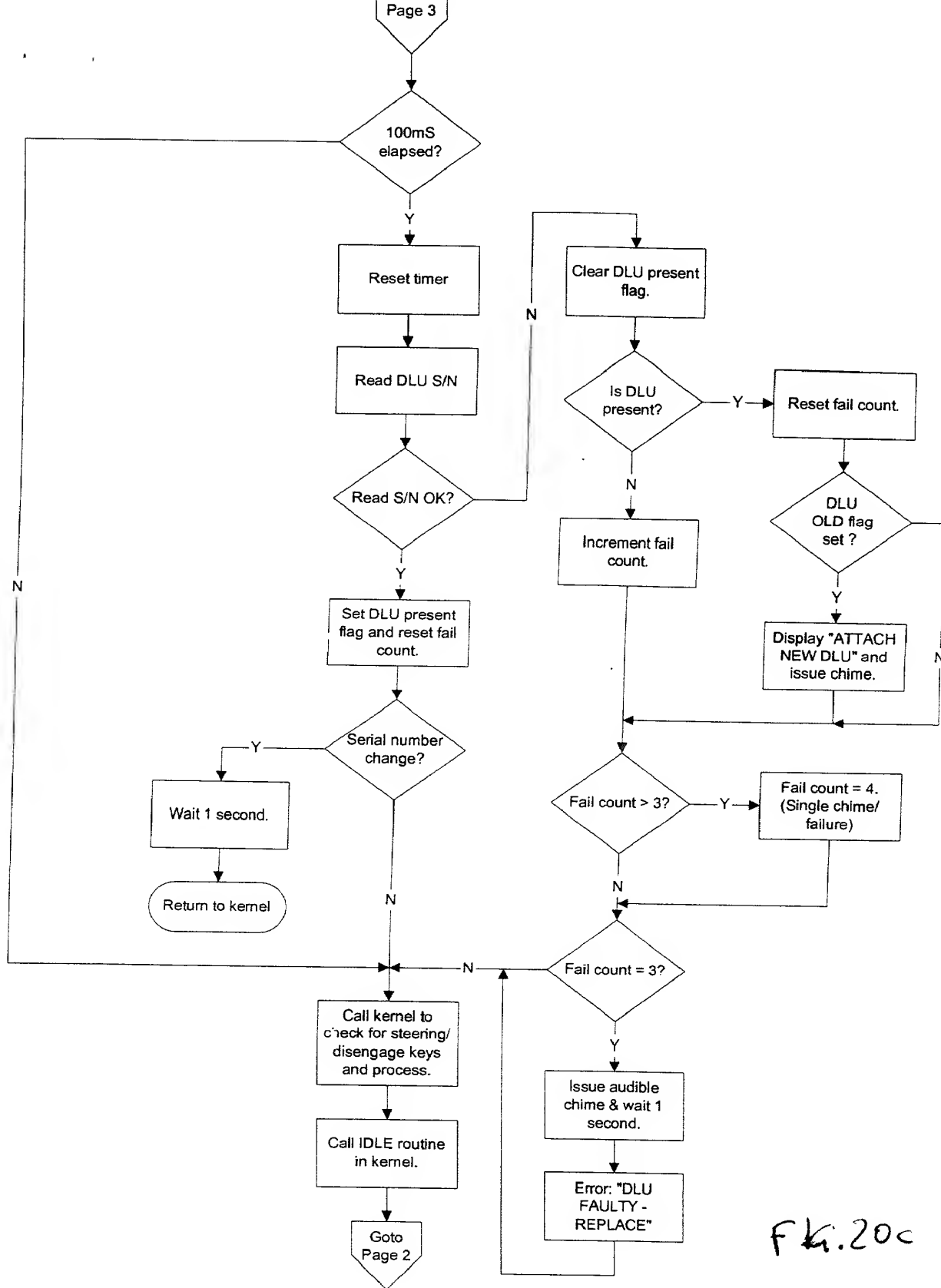
FIG. 20a

0936791.04401
T04T40 T049660

09036784 041703



09036781 041701
FD/F40" T849E360



FK.20c

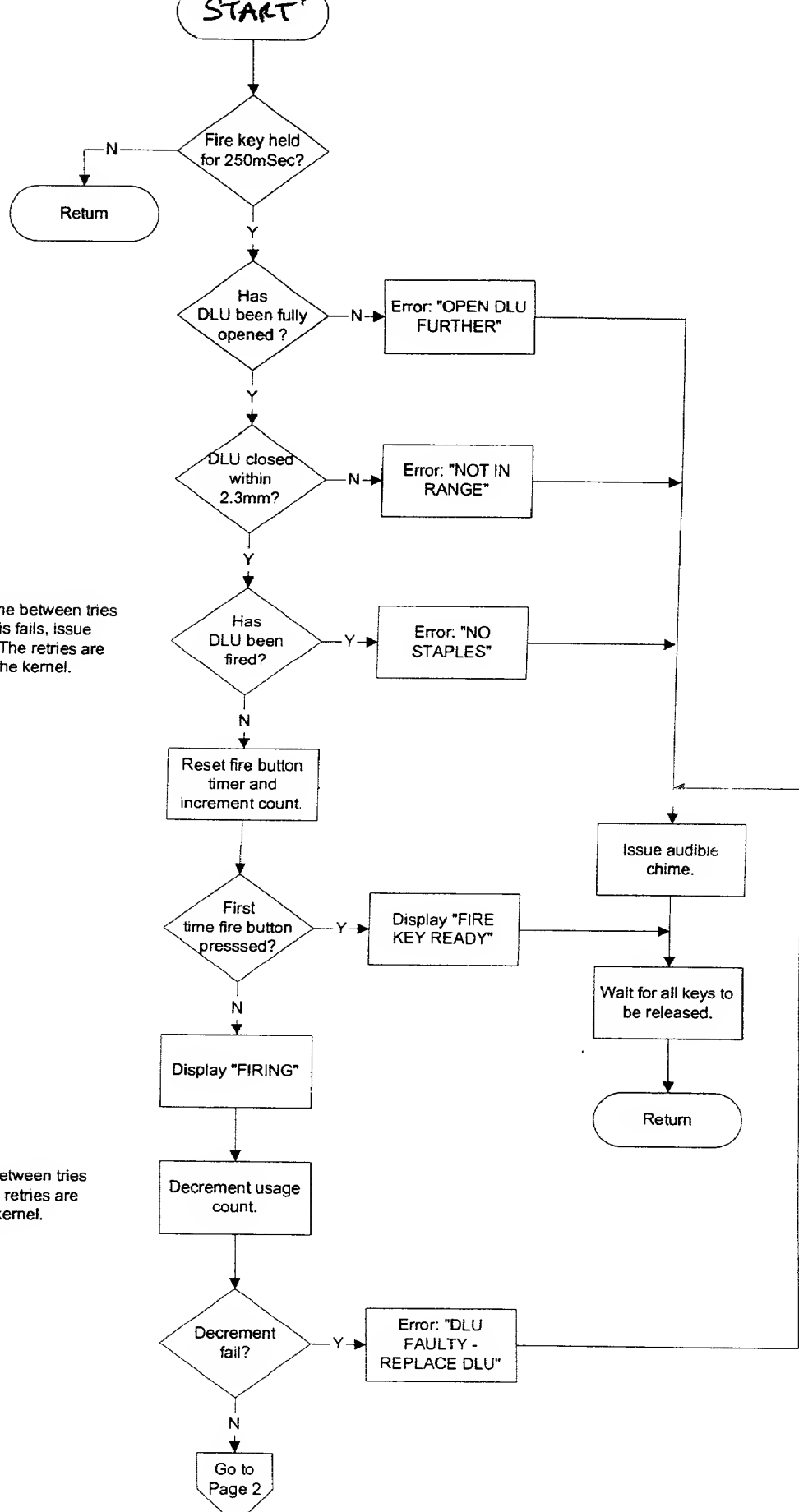
09030701.041701
100740.18000000

nt

*Try 3x w/ 100mS time between tries to read DLU. If this fails, issue message and exit. The retries are performed by the kernel.

*Try 3x w/ 100mS time between tries to decrement DLU. The retries are performed by the kernel.

FIG. 21a



0903678
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02/05/00

Windup threshold is
defined as
SHAFT_WINDUP_MAX

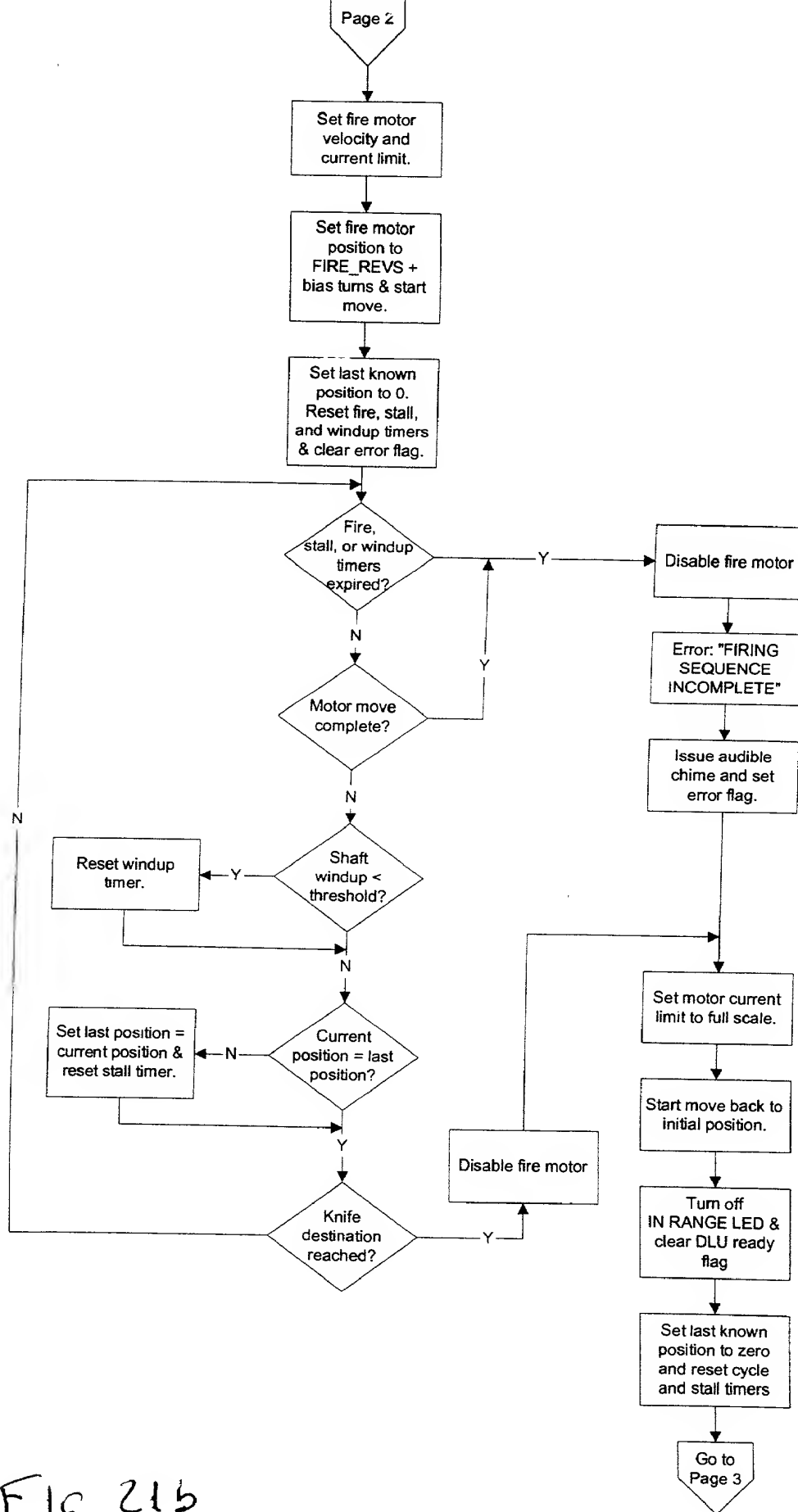
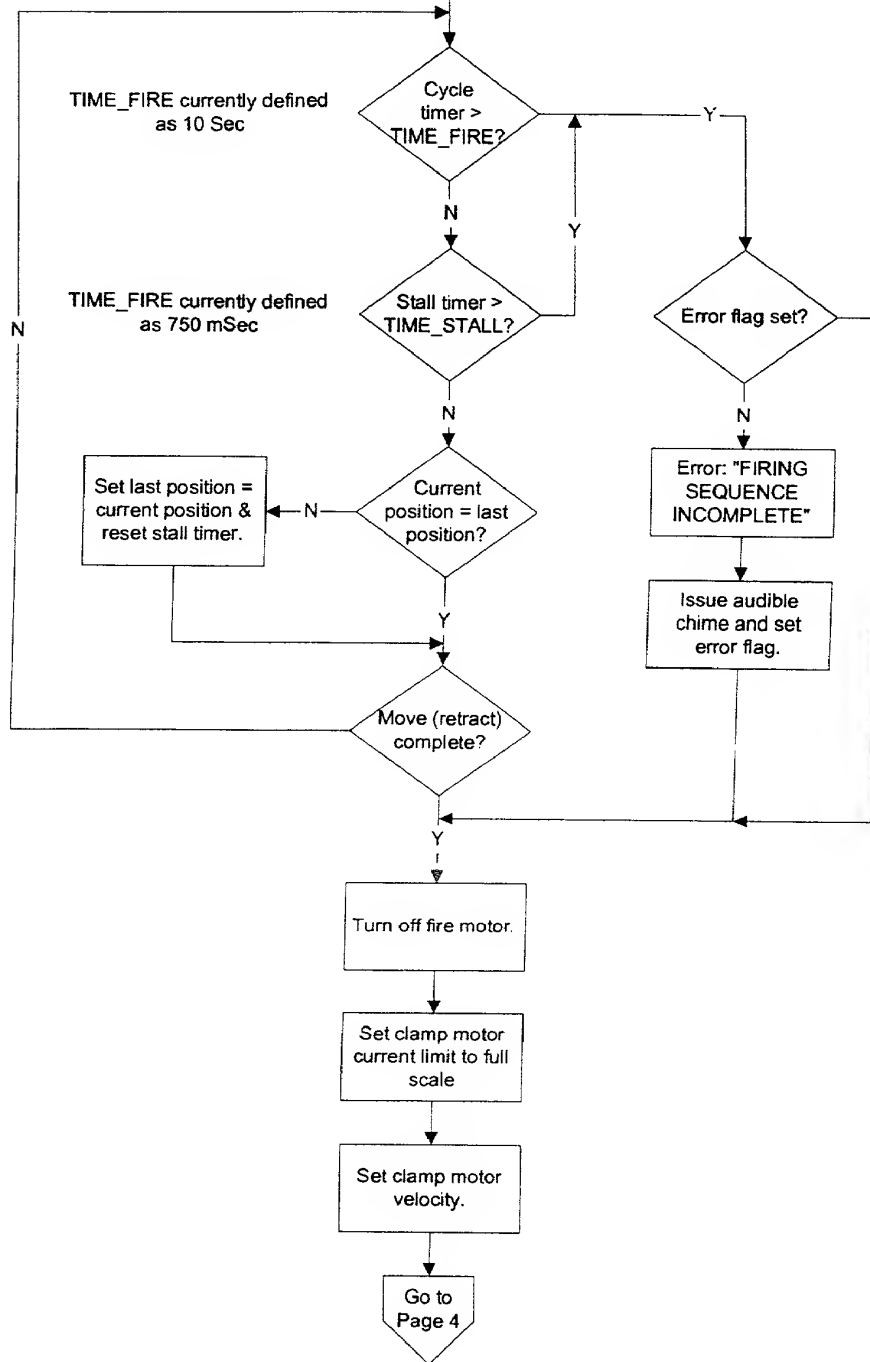


Fig. 21b



0936781.041701

FIG. 21c

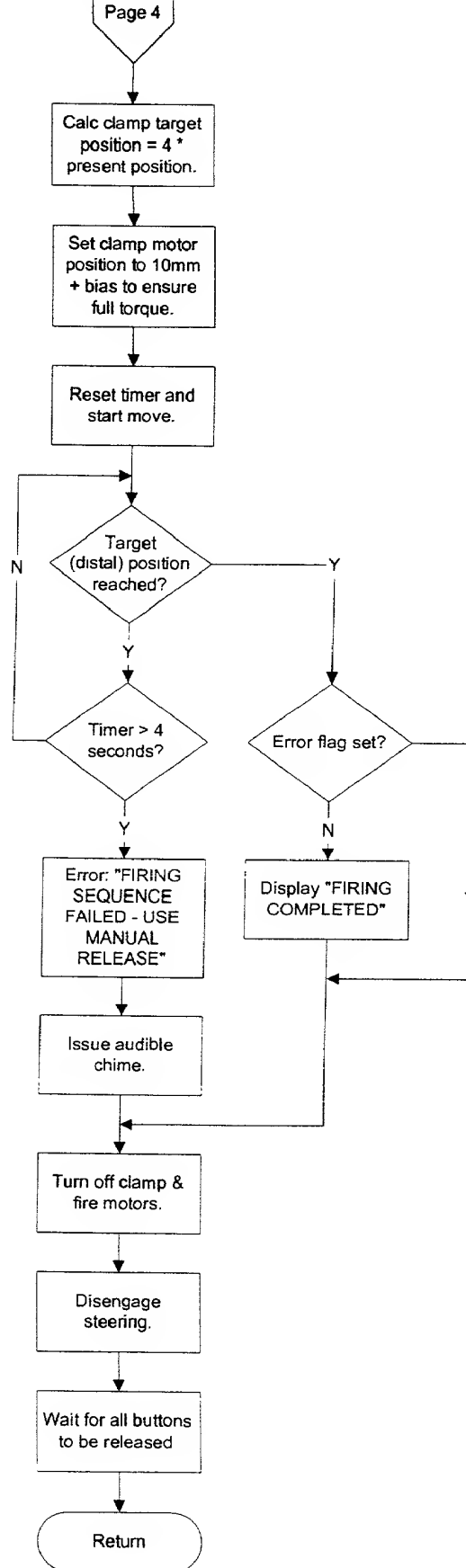
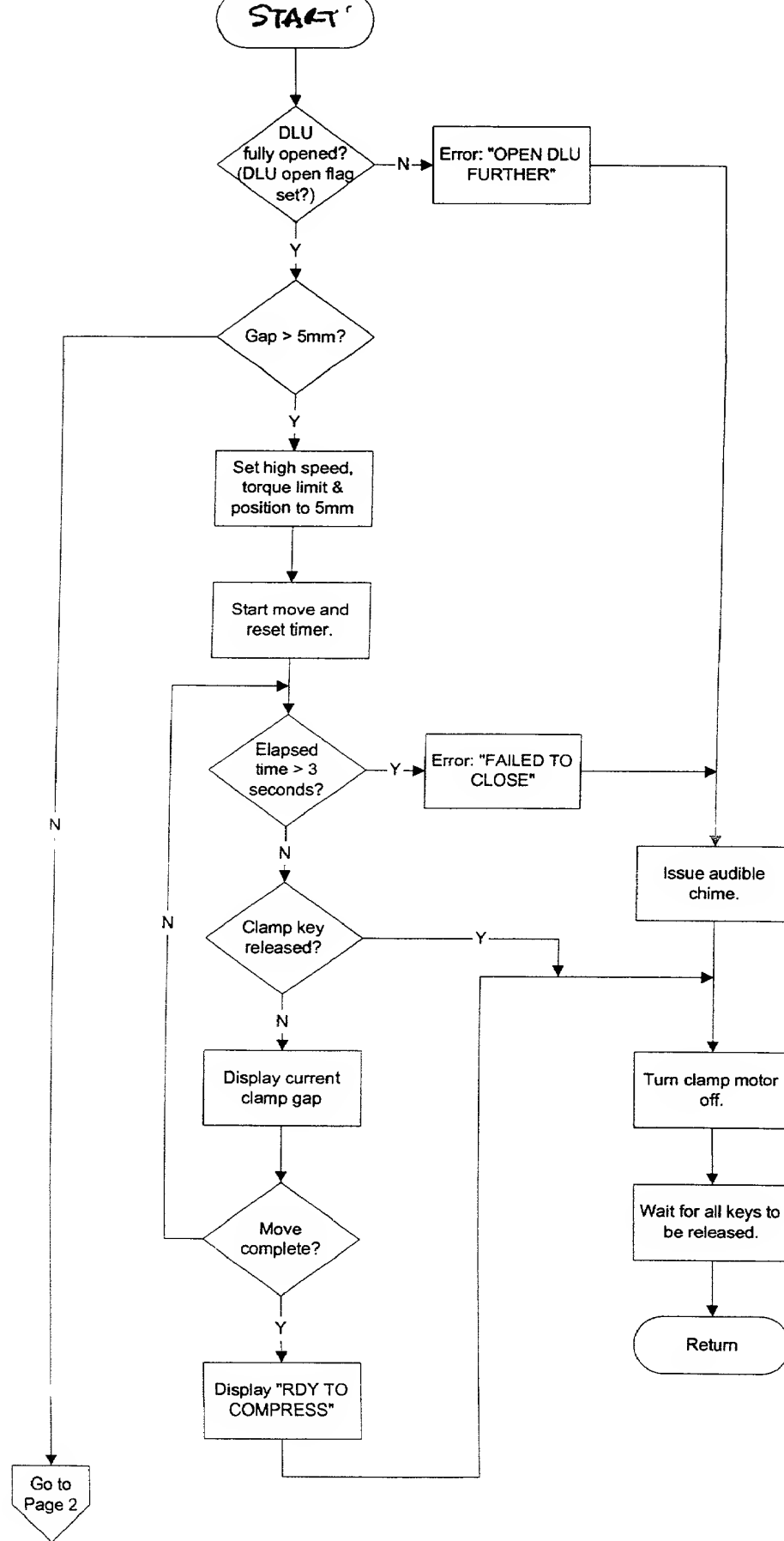


FIG. 21d

09836781.041701

FIG. 22a



09836781.041701

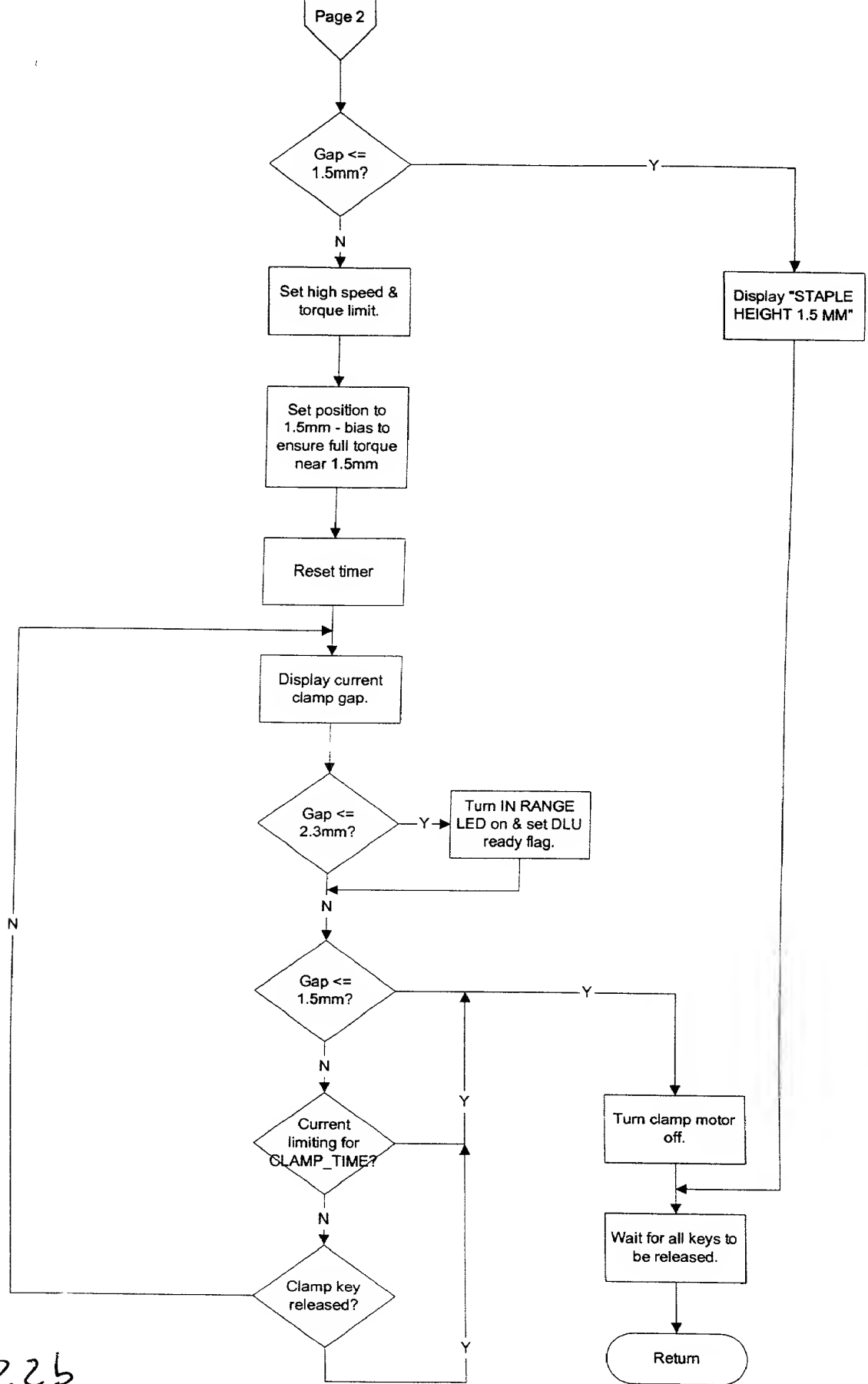
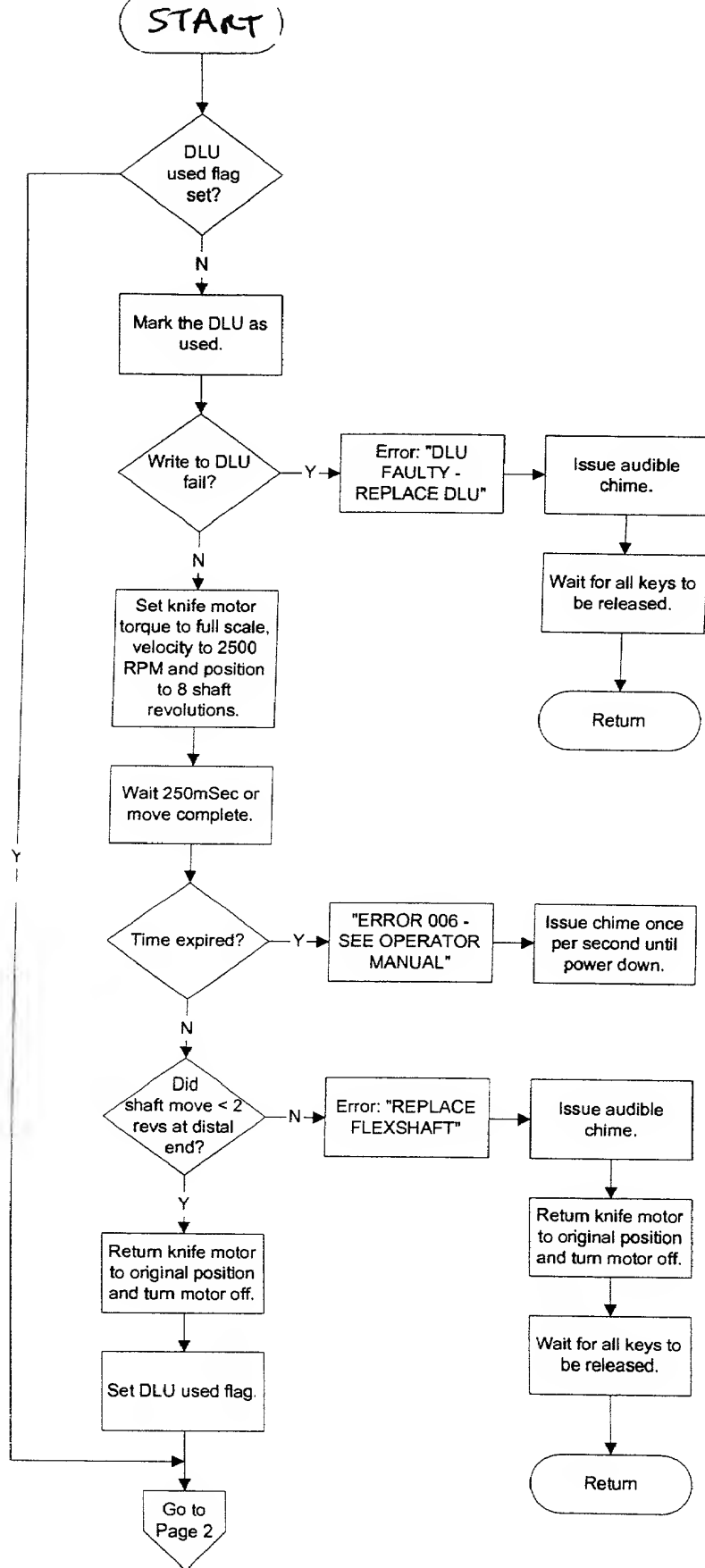


FIG. 22b

09036701.041701

*The kernel tries 3x w/ 100mS time between tries to write DLU.

FIG. 23a



09036784.041704

FIG. 23b

